

Features

- -16V/-7A,
 $R_{DS(ON)} = 13m\Omega(Typ.)@V_{GS}=-4.5V$
 $R_{DS(ON)} = 17m\Omega(Typ.)@V_{GS}=-2.5V$
- Low $R_{DS(ON)}$
- Super High Dense Cell Design
- Reliable and Rugged

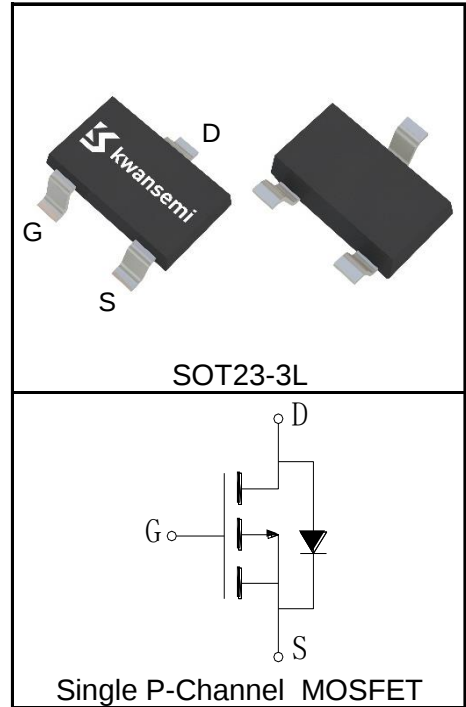
Applications

- Load Switch
- PWM Applications



Halogen-Free

Pin Description



Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (T _A =25°C Unless Otherwise Noted)				
V _{DSS}	Drain-Source Voltage		-16	V
V _{GSS}	Gate-Source Voltage		±12	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	°C
I _S	Diode Continuous Forward Current	T _A =25°C	-1.2	A
Mounted on Large Heat Sink				
I _{DP} ^①	300μs Pulse Drain Current Tested	T _A =25°C	-28	A
I _D ^②	Continuous Drain Current(V _{GS} =-4.5V)	T _A =25°C	-7	A
		T _A =70°C	-5.6	
P _D	Maximum Power Dissipation	T _A =25°C	1.25	W
		T _A =70°C	0.8	
R _{qJL}	Thermal Resistance-Junction to Lead		60	°C/W
R _{θJA} ^③	Thermal Resistance-Junction to Ambient		100	°C/W
Drain-Source Avalanche Ratings				
E _{AS} ^④	Avalanche Energy, Single Pulsed		TBD	mJ

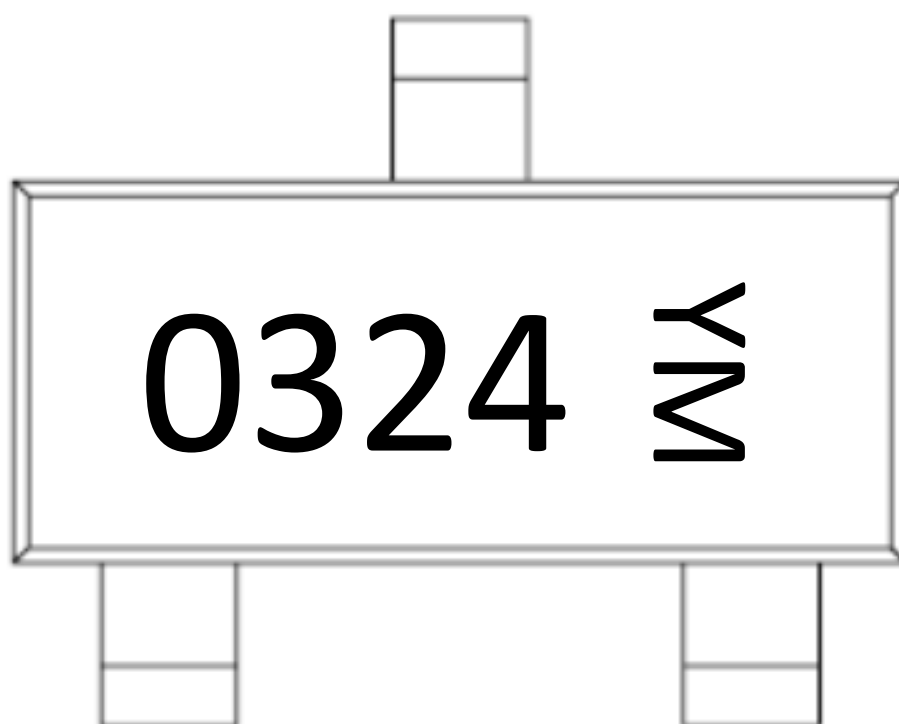
Electrical Characteristics ($T_A=25^{\circ}\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Condition	KS0324EA			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =-250μA	-16	-18		V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-12V, V _{GS} =0V			-1	μA
		T _J =125°C			-100	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =-250μA	-0.4	-0.7	-1	V
I _{GSS}	Gate Leakage Current	V _{GS} =±12V, V _{DS} =0V			±100	nA
R _{DS(ON)} ^⑤	Drain-Source On-state Resistance	V _{GS} =-4.5V, I _{DS} =-6A		13	17	mΩ
		V _{GS} =-2.5V, I _{DS} =-5A		17	23	mΩ
Diode Characteristics						
V _{SD} ^⑤	Diode Forward Voltage	I _{SD} =-6A, V _{GS} =0V		-0.87	-1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} =-6A, dI _{SD} /dt=-100A/μs		13		ns
Q _{rr}	Reverse Recovery Charge			21		nC
Dynamic Characteristics ^⑥						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz		8		Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-8V, Frequency=1.0MHz		1050		pF
C _{oss}	Output Capacitance			170		
C _{rss}	Reverse Transfer Capacitance			105		
t _{d(ON)}	Turn-on Delay Time	V _{DD} =-8V, I _{DS} =-6A, V _{GEN} =-4.5V, R _G =6Ω		13		ns
t _r	Turn-on Rise Time			27		
t _{d(OFF)}	Turn-off Delay Time			38		
t _f	Turn-off Fall Time			12		
Gate Charge Characteristics ^⑥						
Q _g	Total Gate Charge	V _{DS} =-8V, V _{GS} =-4.5V, I _{DS} =-6A		18		nC
Q _{gs}	Gate-Source Charge			4.1		
Q _{gd}	Gate-Drain Charge			5.3		

- Notes:
- ① Pulse width limited by safe operating area.
 - ② Calculated continuous current based on maximum allowable junction temperature. The package limitation current is 6.5A.
 - ③ When mounted on 1 inch square copper board, $t \leq 10\text{sec}$. The value in any given application depends on the user's specific board design.
 - ④ Limited by T_{Jmax} . Starting $T_J = 25^{\circ}\text{C}$.
 - ⑤ Pulse test; Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
 - ⑥ Guaranteed by design, not subject to production testing.

Ordering and Marking Information

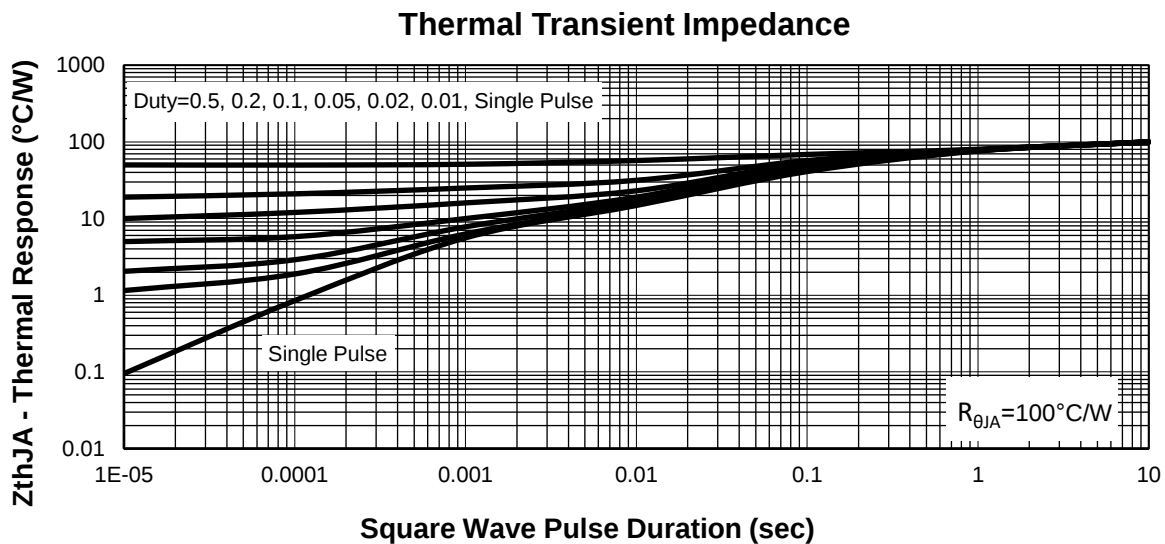
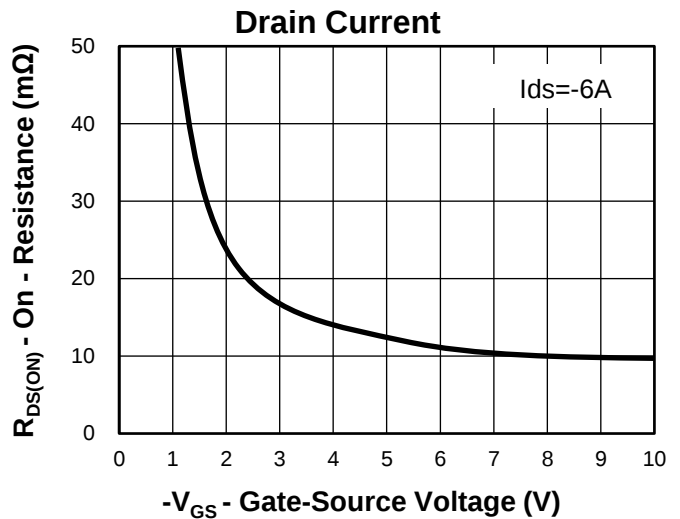
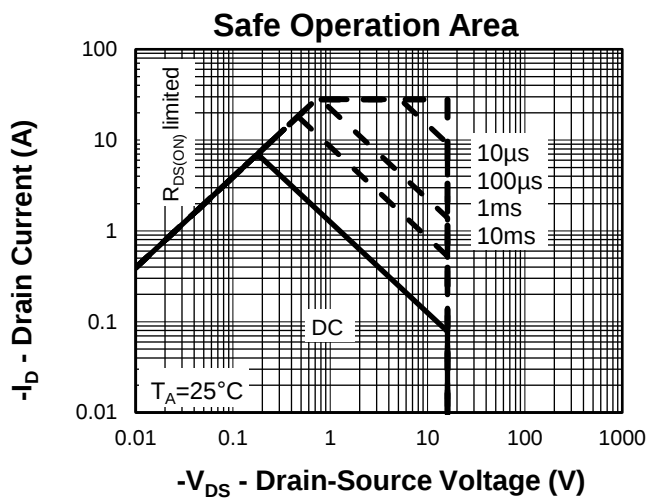
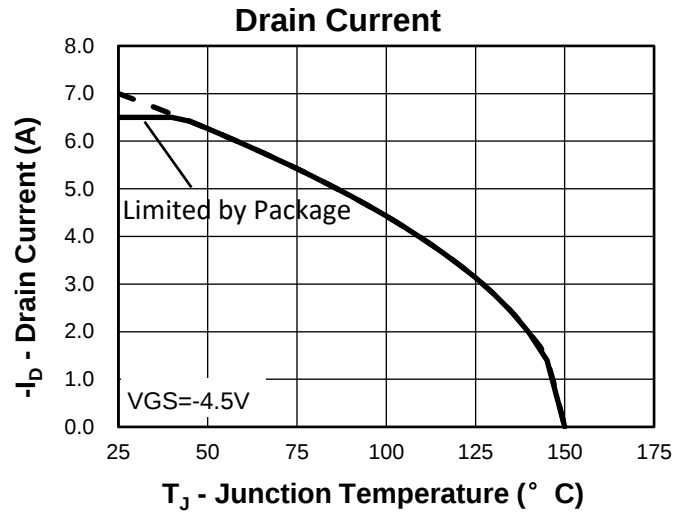
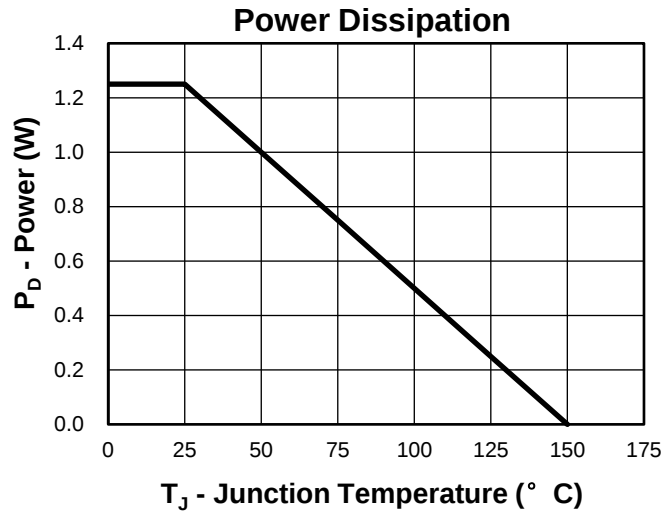
Device	Package	Packaging	Quantity	Reel Size	Tape width
KS0324EA	SOT23-3L	Tape&Reel	3000	7"	8mm



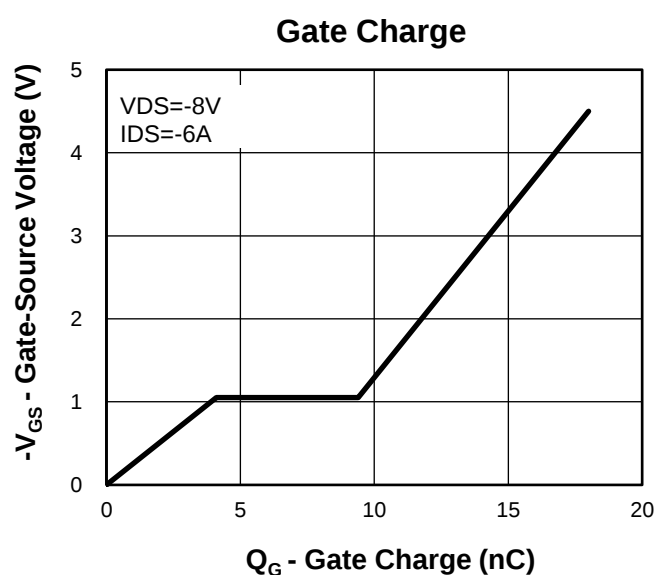
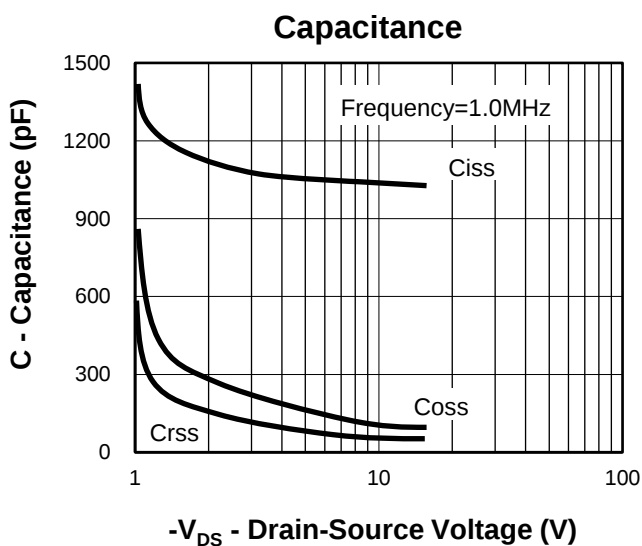
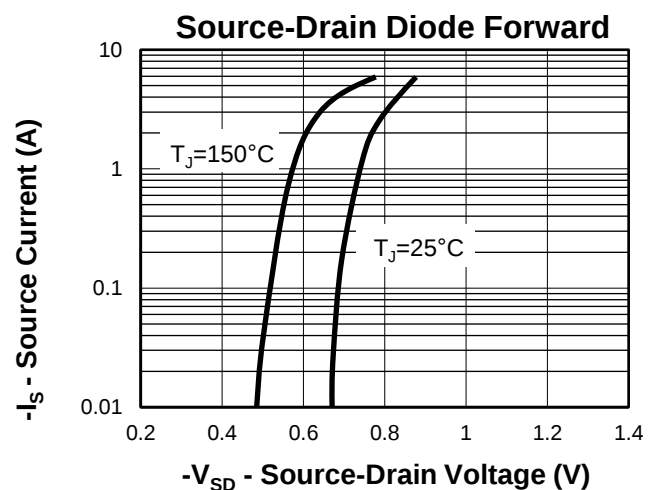
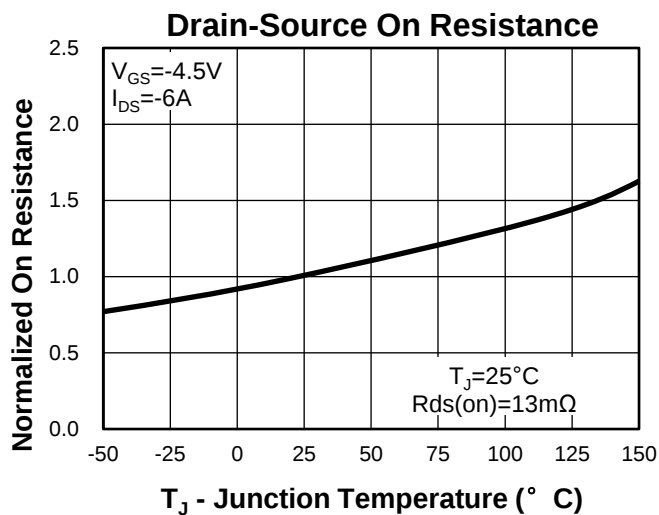
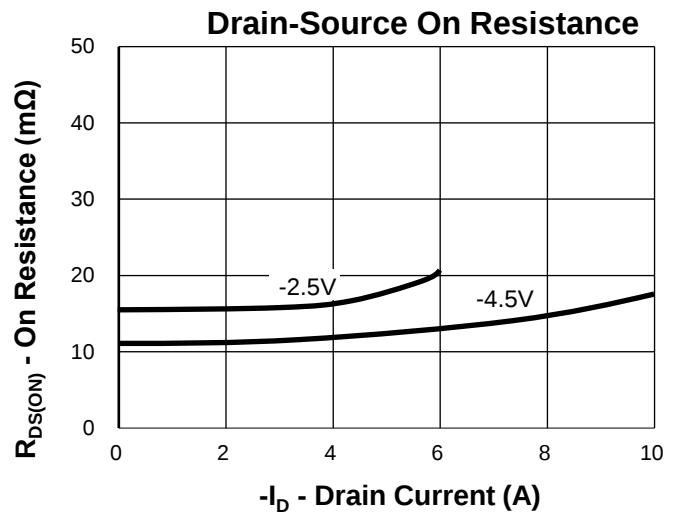
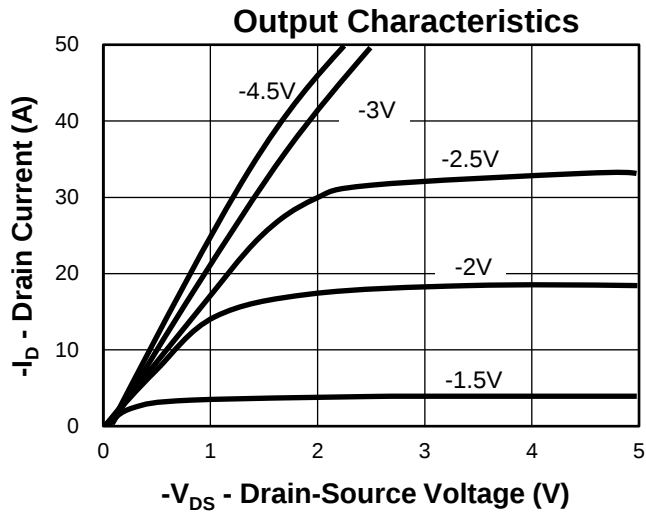
Y =Year,2017-A,2018-B,etc.

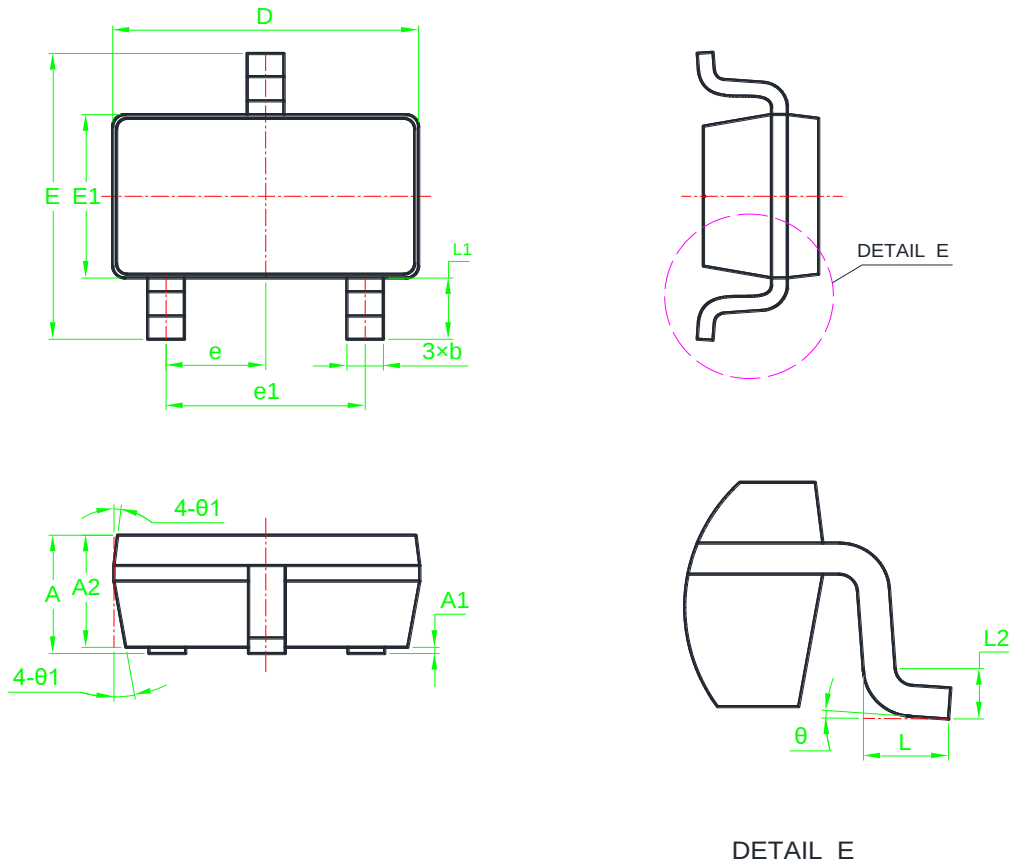
M =Month,Jan-1,Feb-2,....Sep-9,Oct-A,Nov-B,Dec-C.

Typical Characteristics



Typical Characteristics

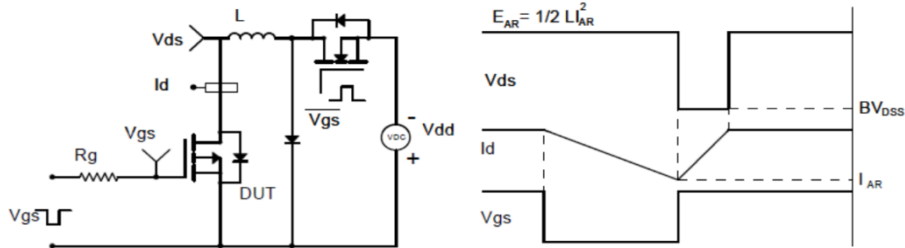


Package Information
SOT23-3L


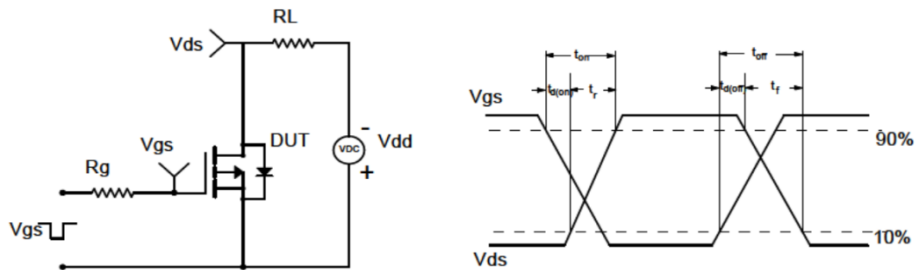
SYMBOL	MM			INCH			SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX		MIN	NOM	MAX	MIN	NOM	MAX
A	1.05	1.15	1.25	0.041	0.045	0.049	e	0.95BSC			0.037BSC		
A1	0.01	*	0.10	0.000	*	0.004	e1	1.9BSC			0.075BSC		
A2	1.05	1.10	1.15	0.041	0.043	0.045	L	0.30	0.45	0.60	0.012	0.018	0.024
b	0.30	0.40	0.50	0.012	0.016	0.020	L1	0.6REF			0.024REF		
D	2.82	2.92	3.02	0.111	0.115	0.119	L2	0.254BSC			0.01BSC		
E	2.65	2.80	2.95	0.104	0.110	0.116	θ	0°	*	8°	0°	*	8°
E1	1.50	1.60	1.70	0.059	0.063	0.067	θ 1	0°	*	10°	0°	*	10°

Note: Dimensions do not inclusive burrs and mold flash.

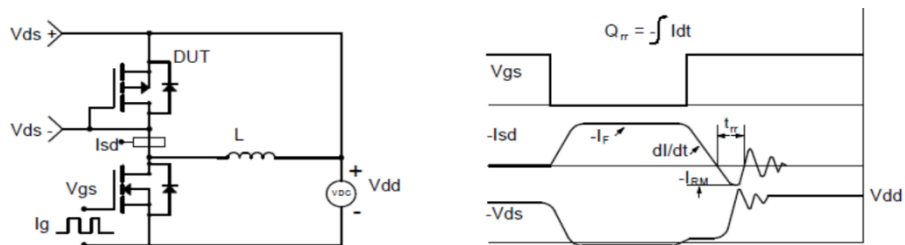
Avalanche Test Circuit and Waveforms



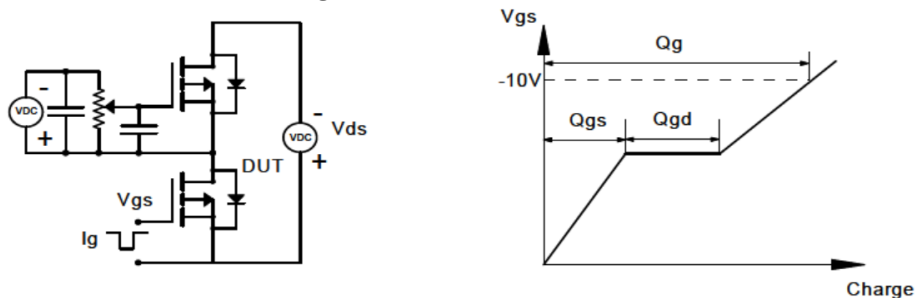
Switching Time Test Circuit and Waveforms



Diode Recovery Test Circuit and Waveforms



Gate Charge Test Circuit and Waveform



Customer Service

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