

Features

- -16V/-13A,
 $R_{DS(ON)} = 10m\Omega(Typ.)@V_{GS}=-4.5V$
 $R_{DS(ON)} = 14m\Omega(Typ.)@V_{GS}=-2.5V$
- Low $R_{DS(ON)}$
- Super High Dense Cell Design
- Reliable and Rugged

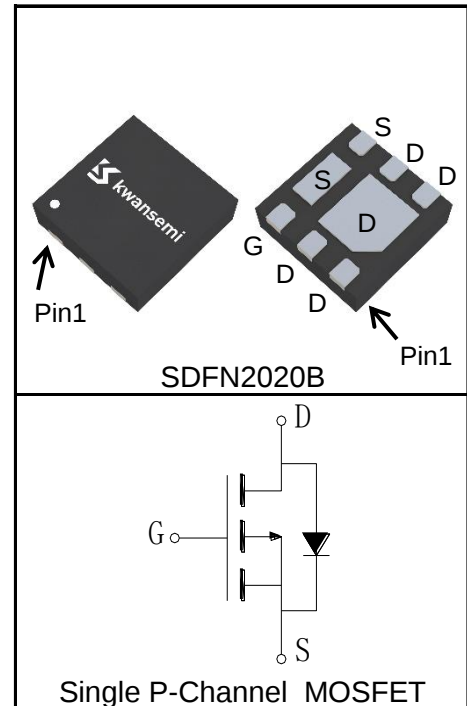
Applications

- Load Switch
- PWM Applications



Halogen-Free

Pin Description



Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (T _A =25°C Unless Otherwise Noted)				
V _{DSS}	Drain-Source Voltage		-16	V
V _{GSS}	Gate-Source Voltage		±12	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	°C
I _S	Diode Continuous Forward Current	T _A =25°C	-2.6	A
Mounted on Large Heat Sink				
I _{DP} ^①	300μs Pulse Drain Current Tested	T _A =25°C	-52	A
I _D ^②	Continuous Drain Current(V _{GS} =-4.5V)	T _A =25°C	-13	A
		T _A =70°C	-10	
P _D	Maximum Power Dissipation	T _A =25°C	2.5	W
		T _A =70°C	1.6	
R _{θJC}	Thermal Resistance-Junction to Case		4.5	°C/W
R _{θJA} ^③	Thermal Resistance-Junction to Ambient		50	°C/W
Drain-Source Avalanche Ratings				
E _{AS} ^④	Avalanche Energy, Single Pulsed		TBD	mJ

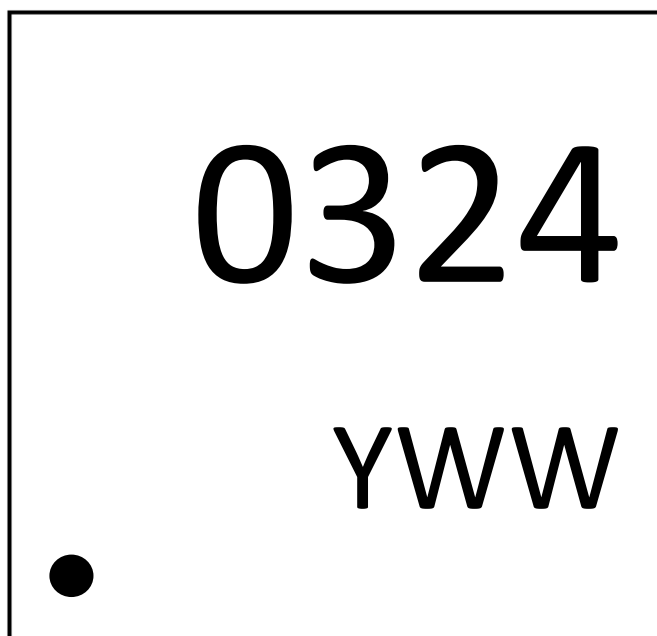
Electrical Characteristics ($T_A=25^{\circ}\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Condition	KS0324UA2			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =-250μA	-16	-18		V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-12V, V _{GS} =0V			-1	μA
		T _J =125°C			-30	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =-250μA	-0.4	-0.7	-1.1	V
I _{GSS}	Gate Leakage Current	V _{GS} =±12V, V _{DS} =0V			±100	nA
R _{DS(ON)} ^⑤	Drain-Source On-state Resistance	V _{GS} =-4.5V, I _{DS} =-8A		10	13	mΩ
		V _{GS} =-2.5V, I _{DS} =-6A		14	20	mΩ
Diode Characteristics						
V _{SD} ^⑤	Diode Forward Voltage	I _{SD} =-8A, V _{GS} =0V		-0.86	-1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} =-8A, dI _{SD} /dt=-100A/μs		14		ns
Q _{rr}	Reverse Recovery Charge			23		nC
Dynamic Characteristics ^⑥						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz		8		Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-8V, Frequency=1.0MHz		1050		pF
C _{oss}	Output Capacitance			170		
C _{rss}	Reverse Transfer Capacitance			105		
t _{d(ON)}	Turn-on Delay Time	V _{DD} =-8V, I _{DS} =-8A, V _{GEN} =-4.5V, R _G =3Ω		14		ns
t _r	Turn-on Rise Time			29		
t _{d(OFF)}	Turn-off Delay Time			40		
t _f	Turn-off Fall Time			13		
Gate Charge Characteristics ^⑥						
Q _g	Total Gate Charge	V _{DS} =-8V, V _{GS} =-4.5V, I _{DS} =-8A		19		nC
Q _{gs}	Gate-Source Charge			4.2		
Q _{gd}	Gate-Drain Charge			5.6		

- Notes:
- ① Pulse width limited by safe operating area.
 - ② Calculated continuous current based on maximum allowable junction temperature.
 - ③ When mounted on 1 inch square copper board, $t \leq 10\text{sec}$. The value in any given application depends on the user's specific board design.
 - ④ Limited by T_{Jmax} . Starting $T_J = 25^{\circ}\text{C}$.
 - ⑤ Pulse test; Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
 - ⑥ Guaranteed by design, not subject to production testing.

Ordering and Marking Information

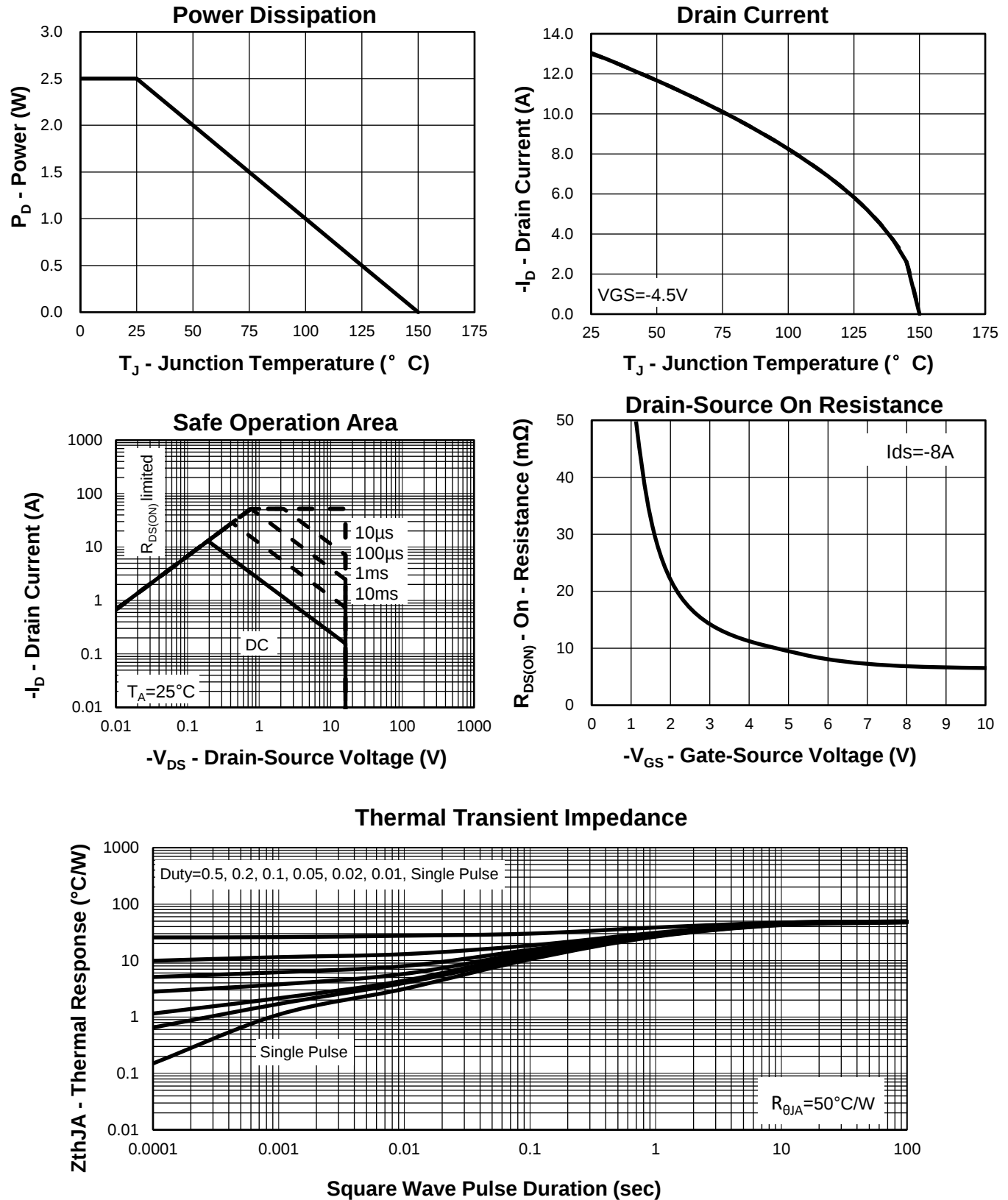
Device	Package	Packaging	Quantity	Reel Size	Tape width
KS0324UA2	SDFN2020B	Tape&Reel	3000	7"	8mm



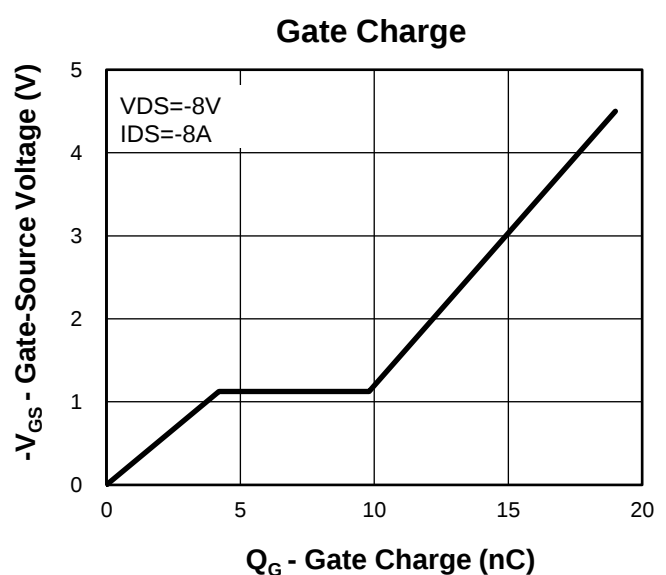
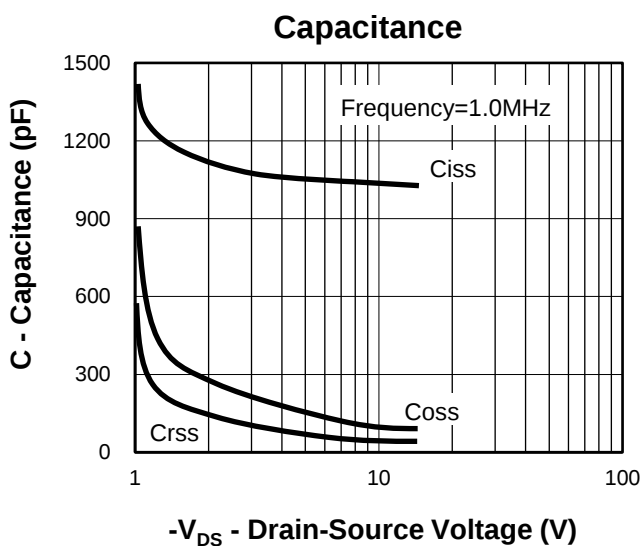
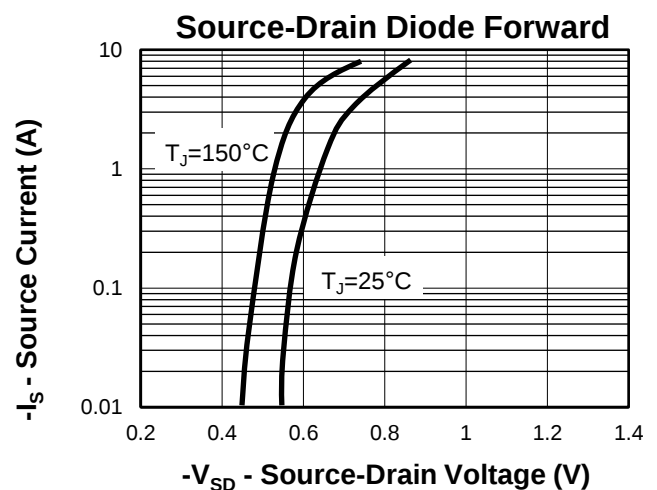
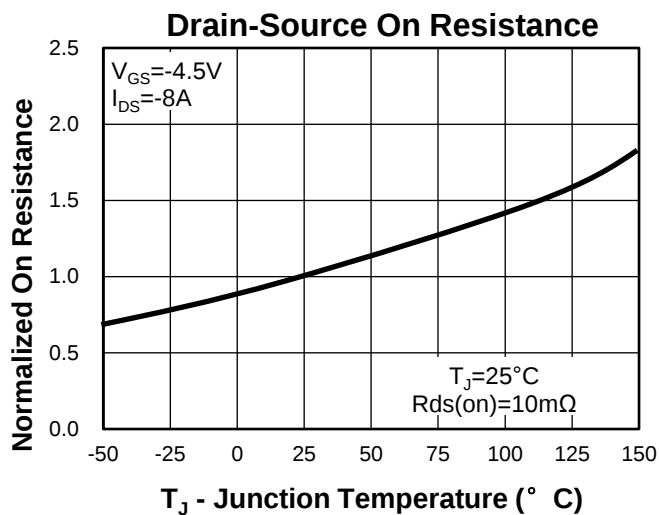
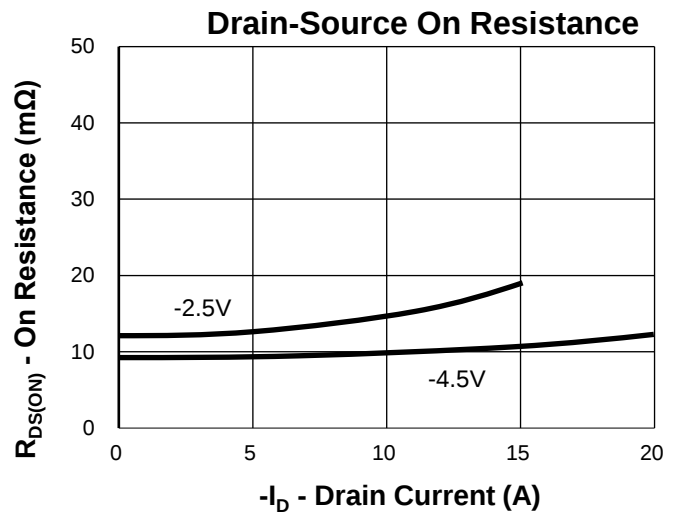
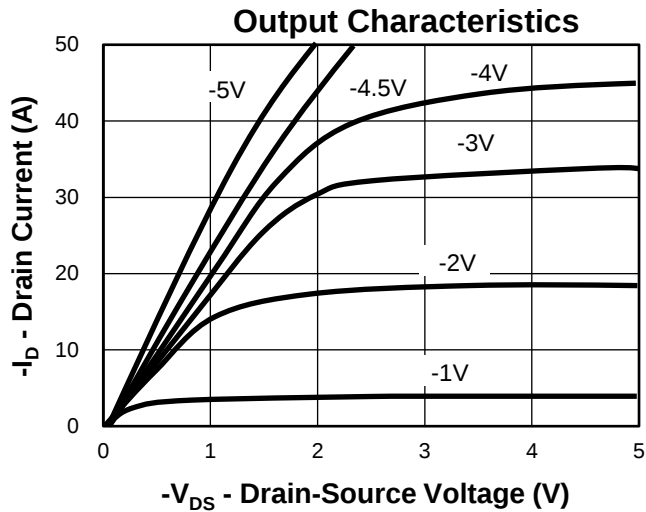
Y =Year,2017-A,2018-B,etc.

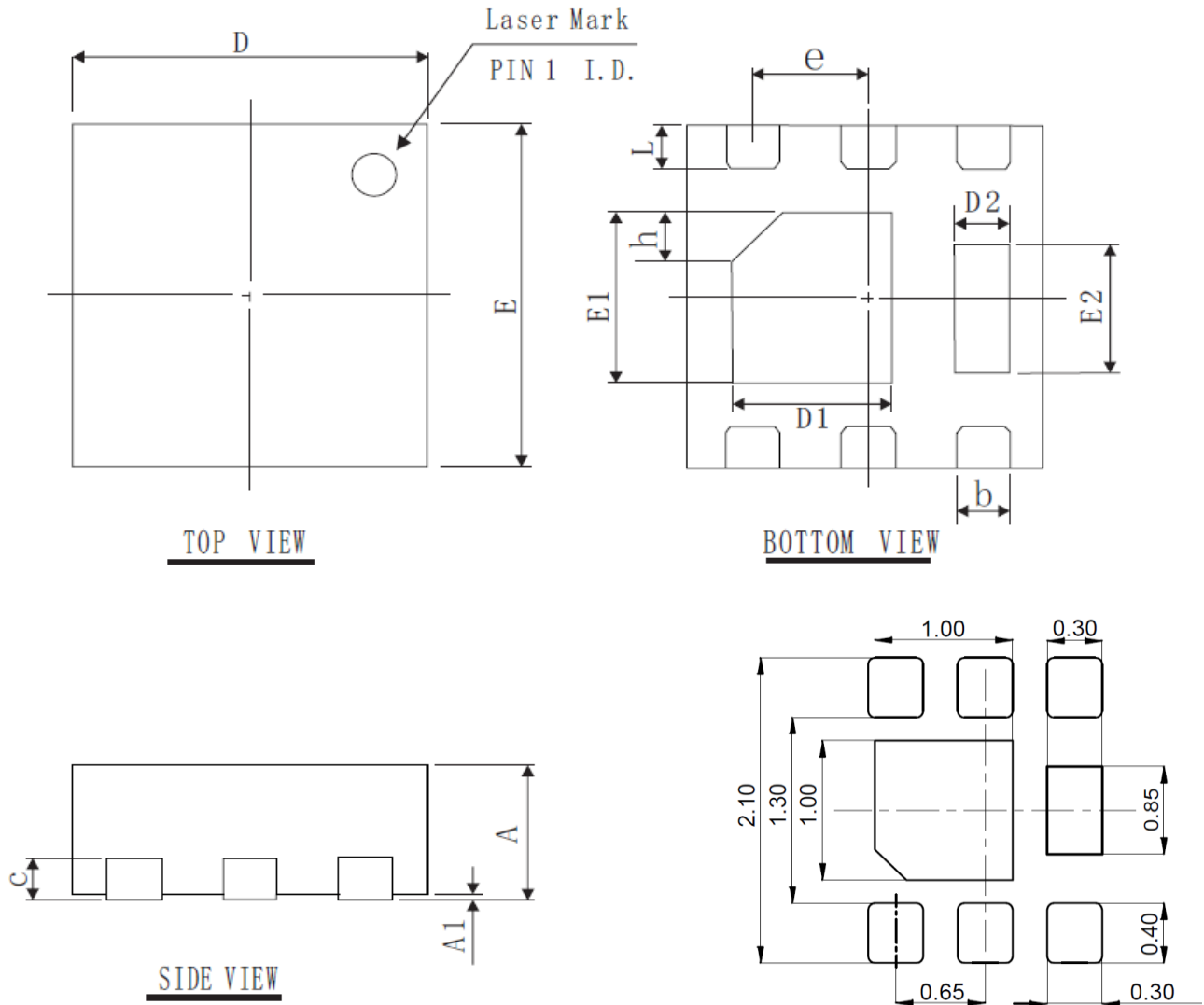
WW =Week.

Typical Characteristics



Typical Characteristics

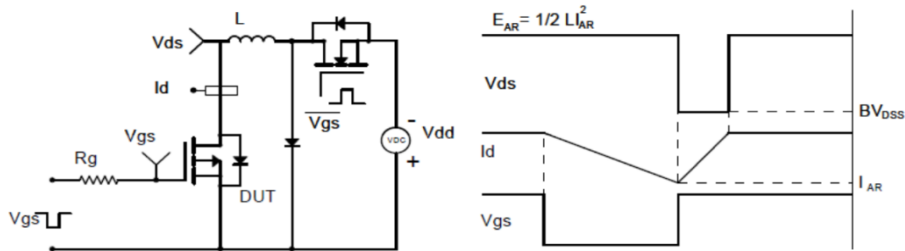


Package Information
SDFN2020B


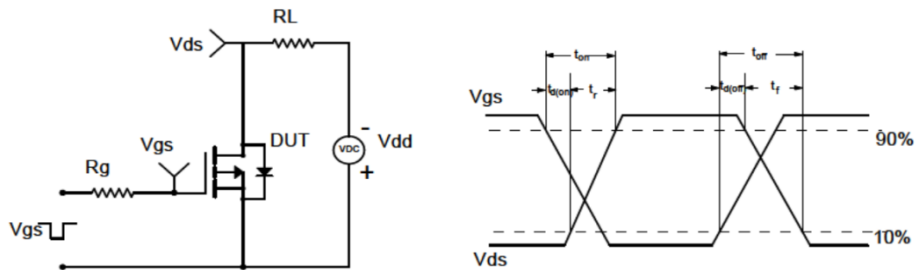
SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.500	0.550	0.600	0.020	0.022	0.024
A1	0.000	0.020	0.050	0.000	0.001	0.002
b	0.250	0.300	0.350	0.010	0.012	0.014
c	0.152 REF			0.006 REF		
D	1.950	2.000	2.050	0.077	0.079	0.081
D1	0.800	0.900	1.000	0.031	0.035	0.039
D2	0.250	0.300	0.350	0.010	0.012	0.014
E	1.950	2.000	2.050	0.077	0.079	0.081
E1	0.800	0.900	1.000	0.031	0.035	0.039
E2	0.460	0.560	0.660	0.018	0.022	0.026
e	0.65 BSC			0.026 BSC		
h	0.200	0.250	0.300	0.008	0.010	0.012
L	0.250	0.300	0.350	0.010	0.012	0.014

Land Pattern
(Only for Reference)

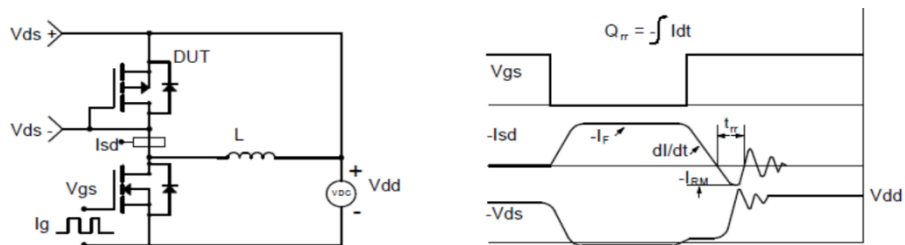
Avalanche Test Circuit and Waveforms



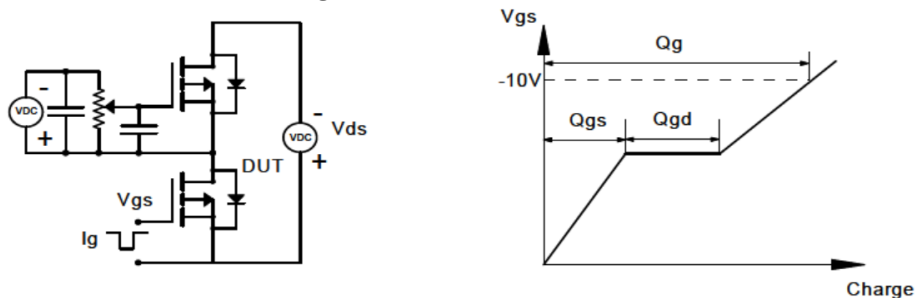
Switching Time Test Circuit and Waveforms



Diode Recovery Test Circuit and Waveforms



Gate Charge Test Circuit and Waveform



Customer Service

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