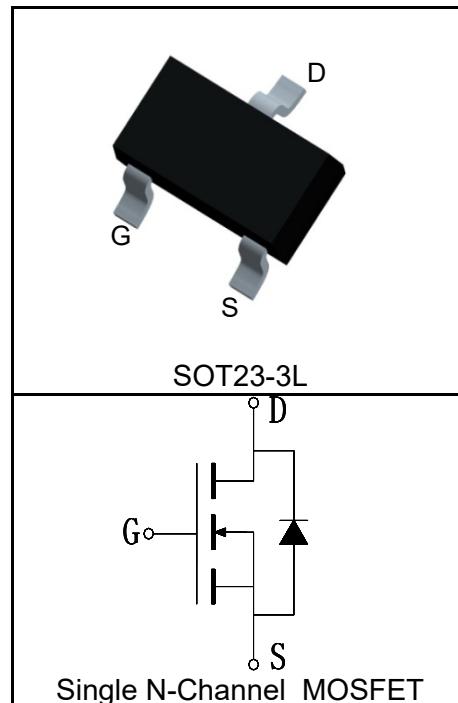


## Features

- 30V/5.8A,
- $R_{DS\ (ON)} = 21m\Omega$ (Typ.)@ $V_{GS}=10V$
- $R_{DS\ (ON)} = 25m\Omega$ (Typ.)@ $V_{GS}=4.5V$
- $R_{DS\ (ON)} = 34m\Omega$ (Typ.)@ $V_{GS}=2.5V$
- Low  $R_{DS\ (ON)}$
- Super High Dense Cell Design
- Reliable and Rugged

## Pin Description



## Applications

- Load Switch



Halogen-Free

## Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
<b>Common Ratings</b> ( $T_A=25^\circ C$ Unless Otherwise Noted)			
$V_{DSS}$	Drain-Source Voltage	30	V
$V_{GSS}$	Gate-Source Voltage	$\pm 12$	
$T_J$	Maximum Junction Temperature	150	$^\circ C$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$I_S$	Diode Continuous Forward Current	$T_A=25^\circ C$	1.5
			A
<b>Mounted on Large Heat Sink</b>			
$I_{DP}^{(1)}$	300 $\mu s$ Pulse Drain Current Tested	$T_A=25^\circ C$	23
$I_D^{(2)}$	Continuous Drain Current( $V_{GS}=10V$ )	$T_A=25^\circ C$	5.8
		$T_A=70^\circ C$	4.6
$P_D$	Maximum Power Dissipation	$T_A=25^\circ C$	1.3
		$T_A=70^\circ C$	0.8
$R_{\theta JC}$	Thermal Resistance-Junction to Case	-	$^\circ C/W$
$R_{\theta JA}^{(3)}$	Thermal Resistance-Junction to Ambient	100	$^\circ C/W$
<b>Drain-Source Avalanche Ratings</b>			
$E_{AS}^{(4)}$	Avalanche Energy, Single Pulsed	TBD	mJ

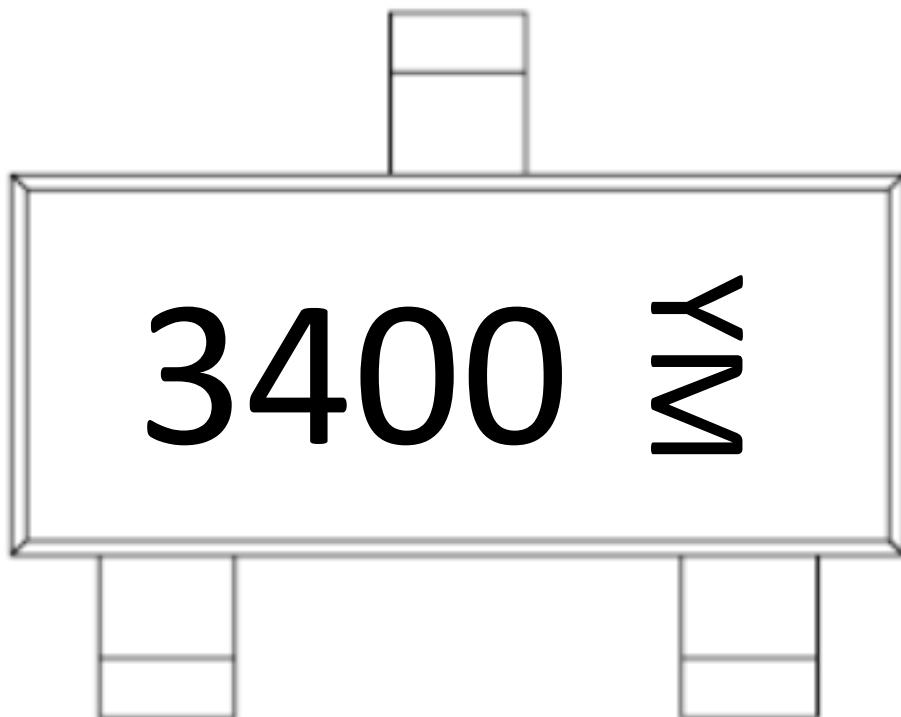
**Electrical Characteristics** ( $T_A=25^\circ\text{C}$  Unless Otherwise Noted)

Symbol	Parameter	Test Condition	KS3400EA			Unit
			Min.	Typ.	Max.	
<b>Static Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{DS}}=250\mu\text{A}$	30			V
$\text{I}_{\text{DSS}}$	Zero Gate Voltage Drain Current	$\text{V}_{\text{DS}}=30\text{V}, \text{V}_{\text{GS}}=0\text{V}$			1	$\mu\text{A}$
		$\text{T}_J=125^\circ\text{C}$			30	
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_{\text{DS}}=250\mu\text{A}$	0.5	0.7	1.4	V
$\text{I}_{\text{GSS}}$	Gate Leakage Current	$\text{V}_{\text{GS}}=\pm 12\text{V}, \text{V}_{\text{DS}}=0\text{V}$			$\pm 100$	nA
$\text{R}_{\text{DS(ON)}}^{(5)}$	Drain-Source On-state Resistance	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_{\text{DS}}=5\text{A}$		21	30	m $\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_{\text{DS}}=4\text{A}$		25	40	m $\Omega$
		$\text{V}_{\text{GS}}=2.5\text{V}, \text{I}_{\text{DS}}=2\text{A}$		34	55	m $\Omega$
<b>Diode Characteristics</b>						
$\text{V}_{\text{SD}}^{(5)}$	Diode Forward Voltage	$\text{I}_{\text{SD}}=3\text{A}, \text{V}_{\text{GS}}=0\text{V}$		0.81	1.2	V
$\text{t}_{\text{rr}}$	Reverse Recovery Time	$\text{I}_{\text{SD}}=3\text{A}, \frac{d\text{I}_{\text{SD}}}{dt}=100\text{A}/\mu\text{s}$		8		ns
$\text{Q}_{\text{rr}}$	Reverse Recovery Charge			2.3		nC
<b>Dynamic Characteristics</b> <sup>(6)</sup>						
$\text{R}_G$	Gate Resistance	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=0\text{V}, \text{F}=1\text{MHz}$		1		$\Omega$
$\text{C}_{\text{iss}}$	Input Capacitance	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=15\text{V}, \text{Frequency}=1.0\text{MHz}$		680		pF
$\text{C}_{\text{oss}}$	Output Capacitance			105		
$\text{C}_{\text{rss}}$	Reverse Transfer Capacitance			70		
$\text{t}_{\text{d(ON)}}$	Turn-on Delay Time	$\text{V}_{\text{DD}}=10\text{V}, \text{I}_{\text{DS}}=3\text{A}, \text{V}_{\text{GEN}}=10\text{V}, \text{R}_G=6\Omega$		4		ns
$\text{t}_{\text{r}}$	Turn-on Rise Time			5		
$\text{t}_{\text{d(OFF)}}$	Turn-off Delay Time			27		
$\text{t}_{\text{f}}$	Turn-off Fall Time			5		
<b>Gate Charge Characteristics</b> <sup>(6)</sup>						
$\text{Q}_g$	Total Gate Charge	$\text{V}_{\text{DS}}=24\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_{\text{DS}}=3\text{A}$		10		nC
$\text{Q}_{\text{gs}}$	Gate-Source Charge			2		
$\text{Q}_{\text{gd}}$	Gate-Drain Charge			3		

- Notes:
- ①Pulse width limited by safe operating area.
  - ②Calculated continuous current based on maximum allowable junction temperature.
  - ③When mounted on 1 inch square copper board,  $t \leq 10\text{sec}$ . The value in any given application depends on the user's specific board design.
  - ④Limited by  $T_{J\max}$ . Starting  $T_J = 25^\circ\text{C}$ .
  - ⑤Pulse test; Pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
  - ⑥Guaranteed by design, not subject to production testing.

**Ordering and Marking Information**

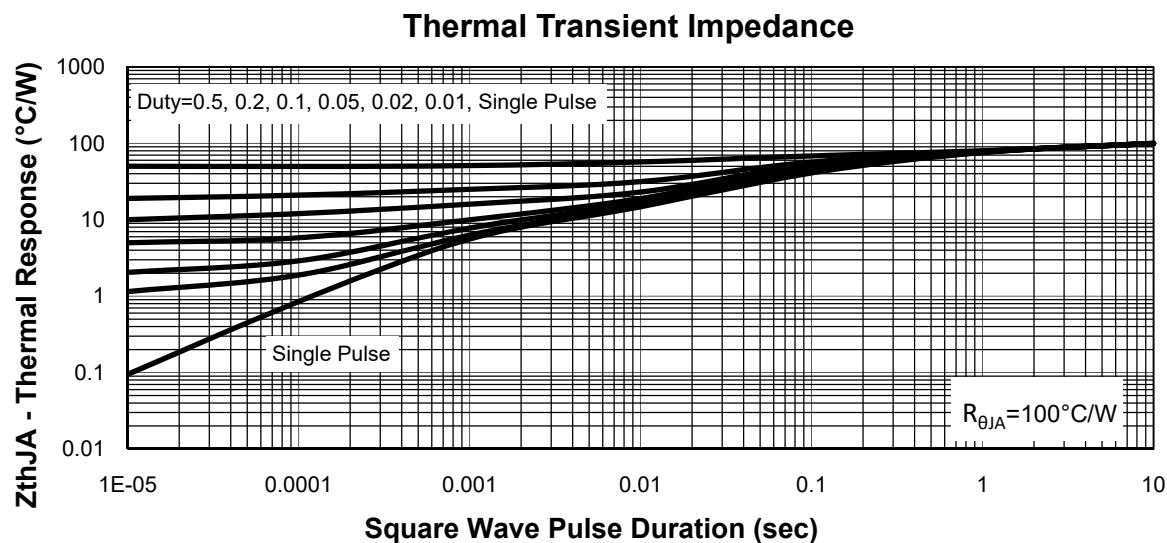
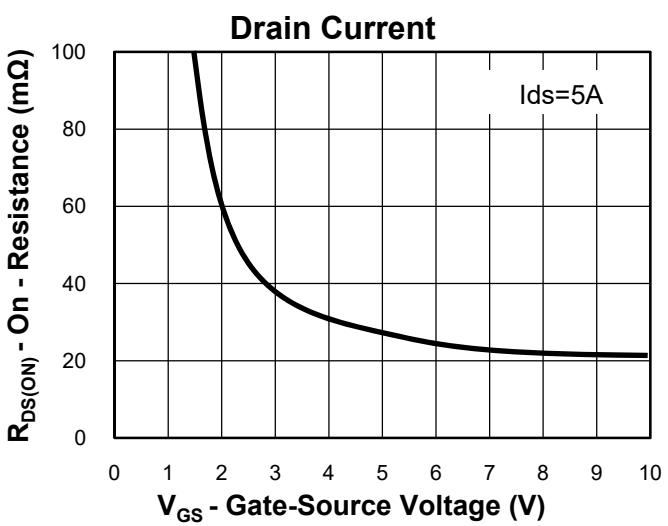
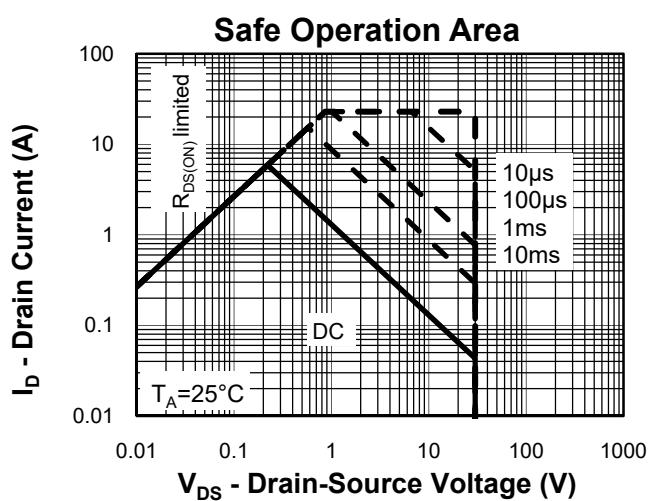
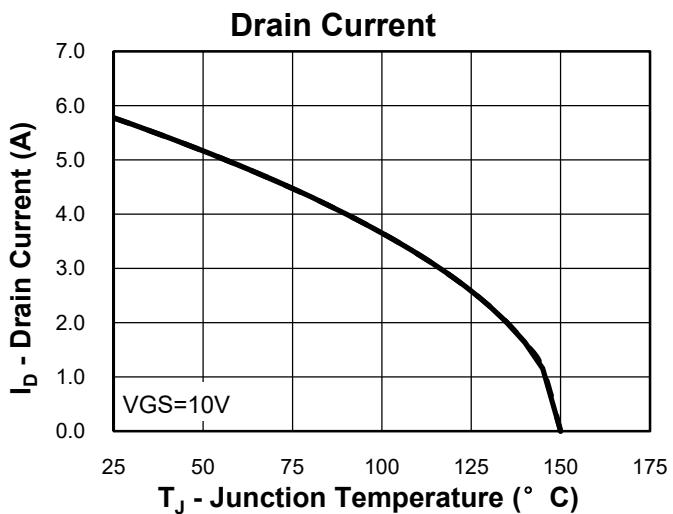
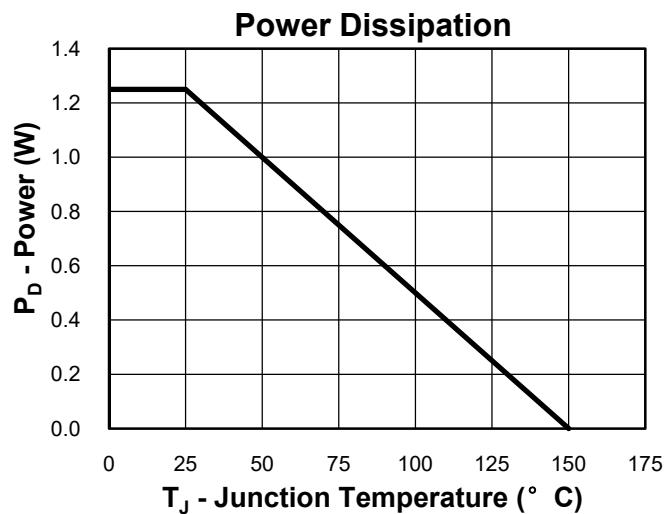
Device	Package	Packaging	Quantity	Reel Size	Tape width
KS3400EA	SOT23-3L	Tape&Reel	3000	7"	8mm



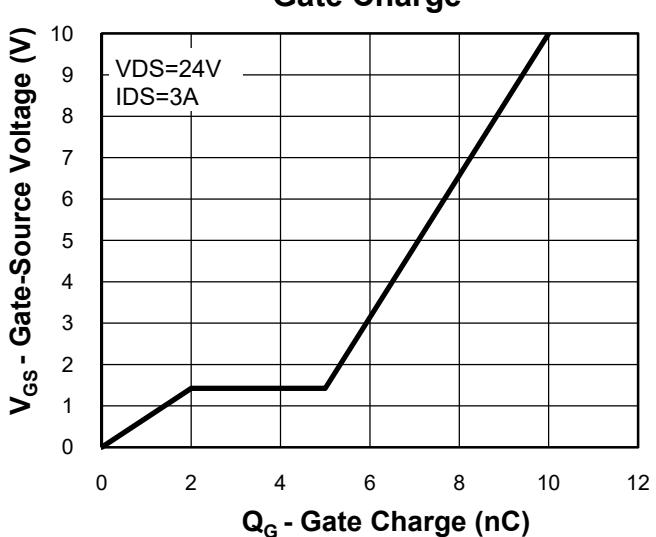
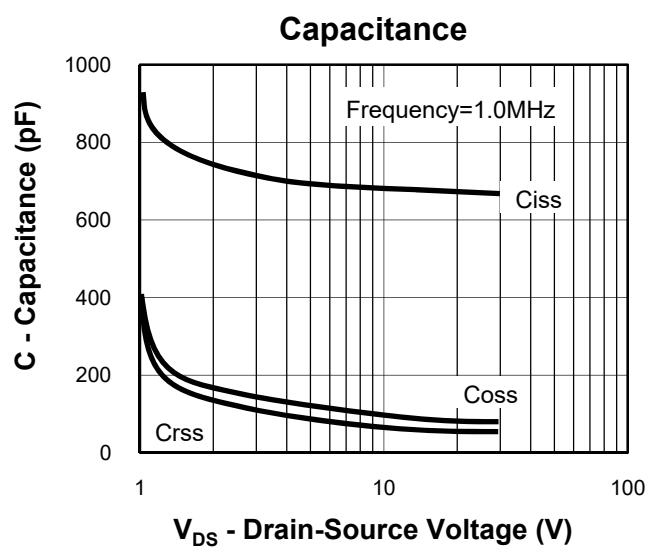
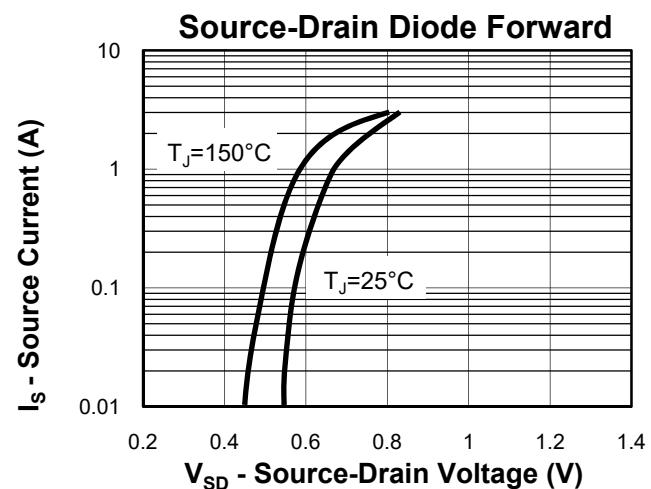
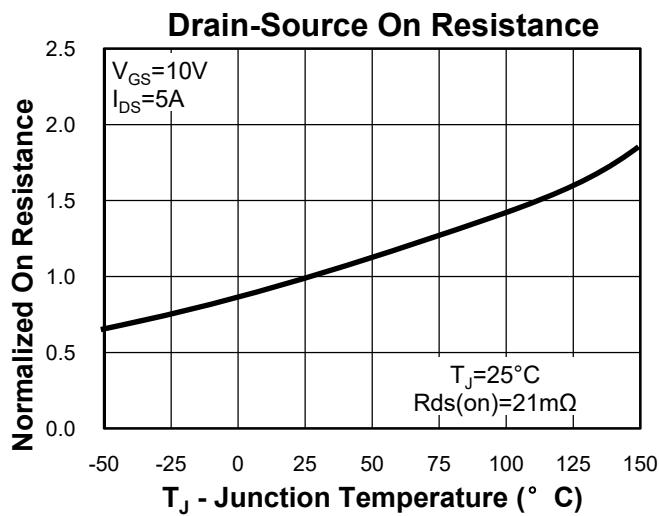
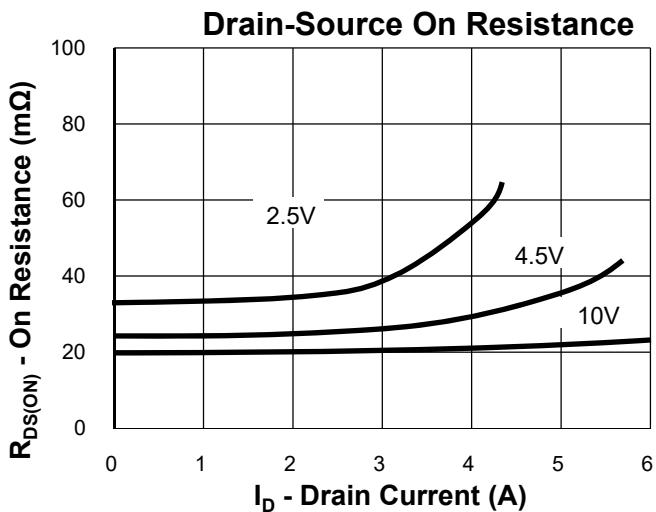
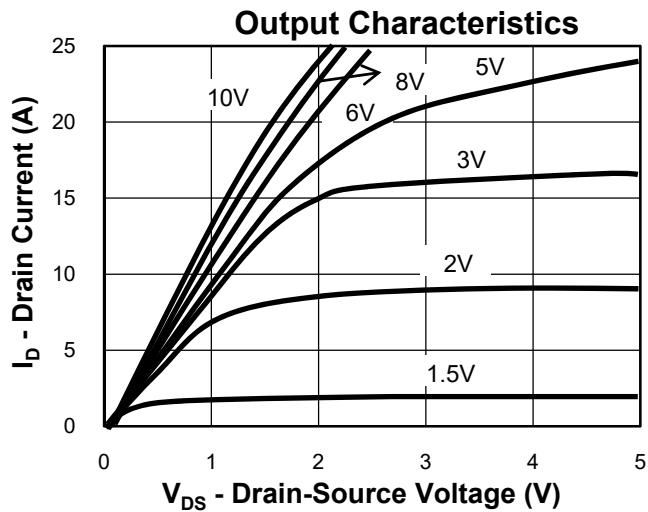
Y =Year,2017-A,2018-B,etc.

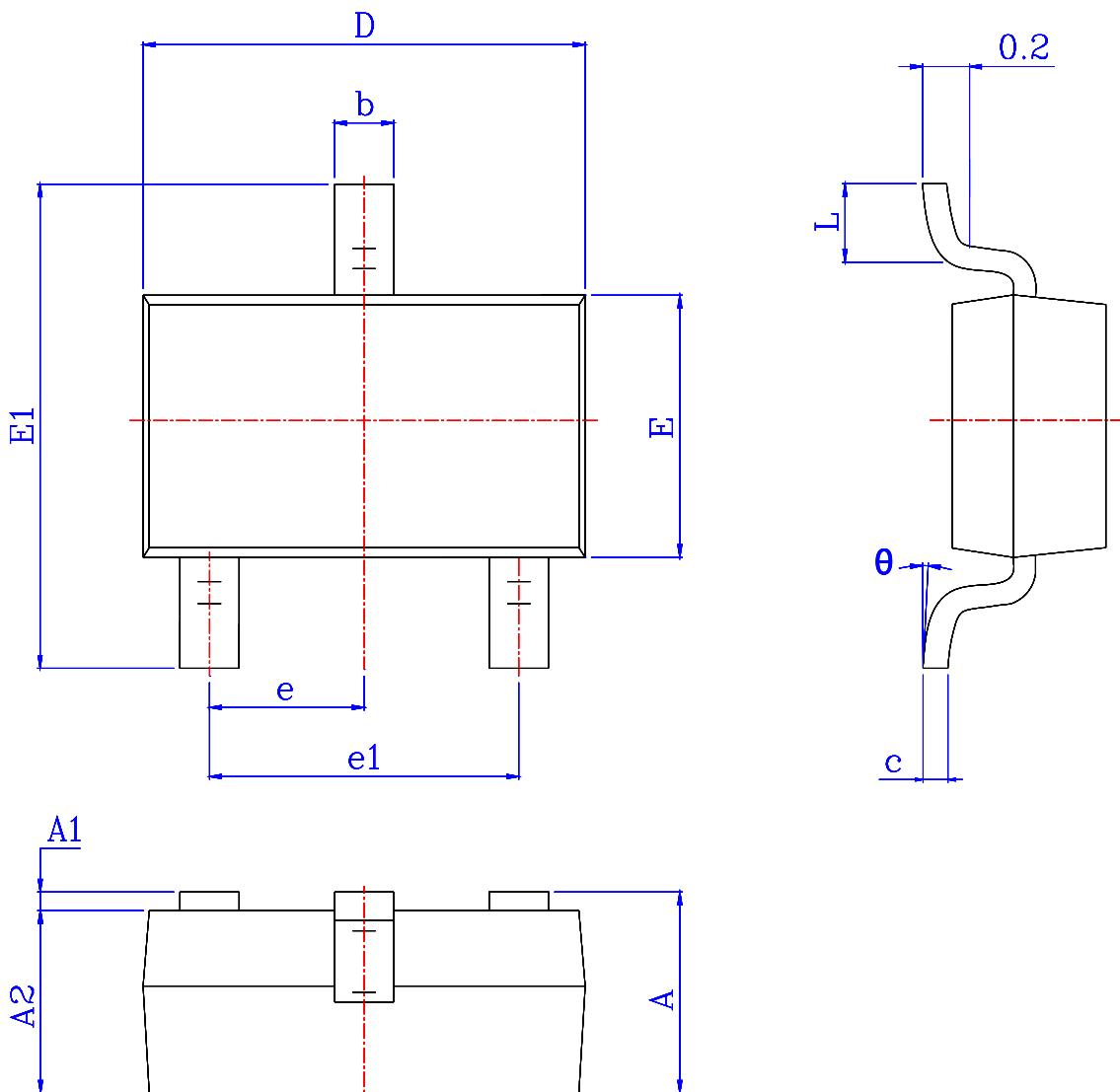
M =Month,Jan-1,Feb-2,...,Sep-9,Oct-A,Nov-B,Dec-C.

## Typical Characteristics



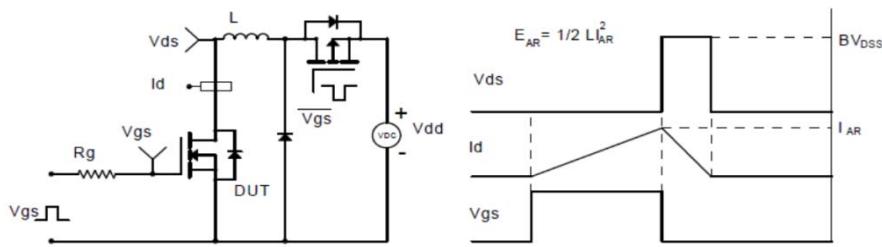
## Typical Characteristics



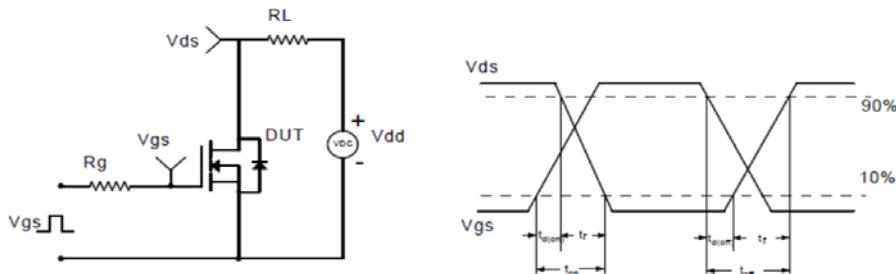
**Package Information**
**SOT23-3L**


SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.950	1.150	1.450	0.037	0.045	0.057
A1	0.000	*	0.150	0.000	*	0.006
A2	0.900	1.100	1.300	0.035	0.043	0.051
b	0.300	0.400	0.500	0.012	0.016	0.020
c	0.080	0.150	0.200	0.003	0.006	0.008
D	2.800	2.925	3.050	0.110	0.115	0.120
E	1.500	1.600	1.750	0.059	0.063	0.069
E1	2.650	2.800	3.000	0.104	0.110	0.118
e	0.950 BSC			0.037 BSC		
e1	1.800	1.900	2.000	0.071	0.075	0.079
L	0.300	0.450	0.600	0.012	0.018	0.024
θ	0°	4°	8°	0°	4°	8°

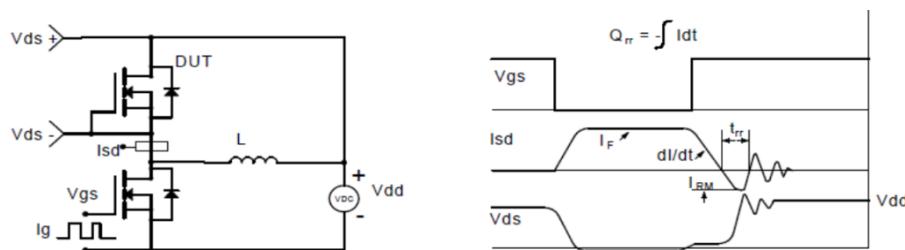
### Avalanche Test Circuit and Waveforms



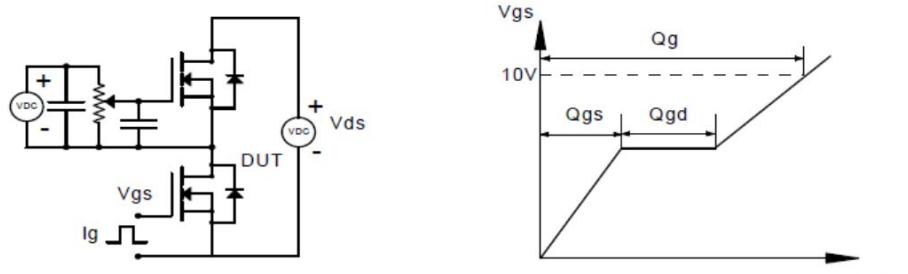
### Switching Time Test Circuit and Waveforms



### Diode Recovery Test Circuit and Waveforms



### Gate Charge Test Circuit and Waveform



### Customer Service

Kwansemi Semiconductor Co.,Ltd

Email:[Sales@kwansemi.com](mailto:Sales@kwansemi.com)

Web:[www.kwansemi.com](http://www.kwansemi.com)

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