KS3628MAT

Complementary Advanced Power MOSFET

Features

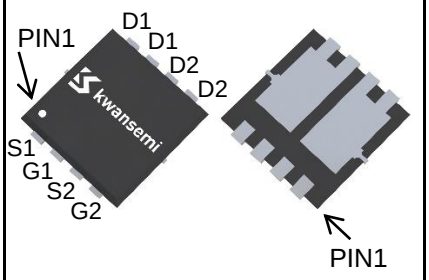
• N-Channel
30V/31A,
$R_{DS\ (ON)}$ =8mΩ(Typ.) @ V_{GS}=10V
$R_{DS\ (ON)}$ =12mΩ(Typ.) @ V_{GS}=4.5V
• P-Channel
-30V/-21A,
$R_{DS\ (ON)}$ =19mΩ (Typ.) @ V_{GS}=-10V
$R_{DS\ (ON)}$ =31mΩ (Typ.) @ V_{GS}=-4.5V
• Very low on-resistance
• Fast Switching

Applications

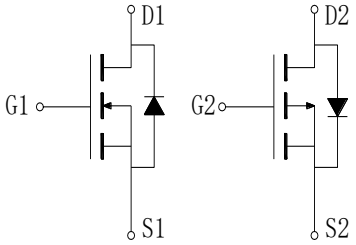
• Motor Drive Applications

RoHS Pb HF Halogen-Free

Pin Description



PDFN3333 DP1



Complementary MOSFET

Absolute Maximum Ratings

Symbol	Parameter		N-Channel	P-Channel	Unit
Common Ratings (T_A=25°C Unless Otherwise Noted)					
V_{DSS}	Drain-Source Voltage		30	-30	V
V_{GSS}	Gate-Source Voltage		±20	±20	
T_J	Maximum Junction Temperature		150	150	°C
T_{STG}	Storage Temperature Range		-55 to 150	-55 to 150	°C
I_S	Diode Continuous Forward Current	T_C=25°C	31	-21	A
Mounted on Large Heat Sink					
I_{DP} ①	300μs Pulse Drain Current Tested	T_C=25°C	124	-84	A
I_D ②	Continuous Drain Current@T_C(V_{GS}=±10V)	T_C=25°C	31	-21	A
		T_C=100°C	20	-13	
	Continuous Drain Current@T_A(V_{GS}=±10V) ③	T_A=25°C	13	-8	
		T_A=70°C	10	-7	
P_D	Maximum Power Dissipation@T_C	T_C=25°C	16	18	W
		T_C=100°C	6.4	7.4	
	Maximum Power Dissipation@T_A ③	T_A=25°C	2.8	2.8	
		T_A=70°C	1.8	1.8	
$R_{\theta JC}$	Thermal Resistance-Junction to Case		7.8	6.8	°C/W
$R_{\theta JA}$ ③	Thermal Resistance-Junction to Ambient		45	45	°C/W
Drain-Source Avalanche Ratings					
E_{AS} ④	Avalanche Energy, Single Pulsed		12	16	mJ

Electrical Characteristics (T_A=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Condition		KS3628MAT			Unit
				Min.	Typ.	Max.	
Static Characteristics							
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_{DS}=250μA	N	30			V
		V_{GS}=0V, I_{DS}=-250μA	P	-30			
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=30V, V_{GS}=0V	N			1	μA
		T_J=125°C				30	
		V_{DS}=-30V, V_{GS}=0V	P			-1	
		T_J=125°C				-30	
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_{DS}=250μA	N	1.1	1.7	2.1	V
		V_{DS}=V_{GS}, I_{DS}=-250μA	P	-1.1	-1.7	-2.1	
I_{GSS}	Gate Leakage Current	V_{GS}=±20V, V_{DS}=0V	N			±100	nA
		V_{GS}=±20V, V_{DS}=0V	P			±100	
$R_{DS(ON)}$ ⑤	Drain-Source On-state Resistance	V_{GS}=10V, I_{DS}=12A	N		8	9.5	mΩ
		V_{GS}=-10V, I_{DS}=-12A	P		19	24	
		V_{GS}=4.5V, I_{DS}=10A	N		12	16	
		V_{GS}=-4.5V, I_{DS}=-10A	P		31	41	
Diode Characteristics							
V_{SD} ⑤	Diode Forward Voltage	I_{SD}=12A, V_{GS}=0V	N		0.85	1.2	V
		I_{SD}=-12A, V_{GS}=0V	P		-0.92	-1.2	
t_{rr}	Reverse Recovery Time	N-Channel I_{SD}=8A, dI_{SD}/dt=100A/μs	N		12		ns
			P		16		
Q_{rr}	Reverse Recovery Charge	P-Channel I_{SD}=-8A, dI_{SD}/dt=100A/μs	N		21		nC
			P		28		
Dynamic Characteristics ⑥							
R_G	Gate Resistance	V_{GS}=0V,V_{DS}=0V,F=1MHz	N		0.8		Ω
			P		4.5		
C_{iss}	Input Capacitance	N-Channel V_{GS}=0V,V_{DS}=15V, Frequency=1.0MHz	N		555		pF
			P		1240		
C_{oss}	Output Capacitance	P-Channel V_{GS}=0V,V_{DS}=-15V, Frequency=1.0MHz	N		260		
			P		205		
C_{rss}	Reverse Transfer Capacitance		N		25		
			P		120		

Electrical Characteristics (T_A=25°C Unless Otherwise Noted)

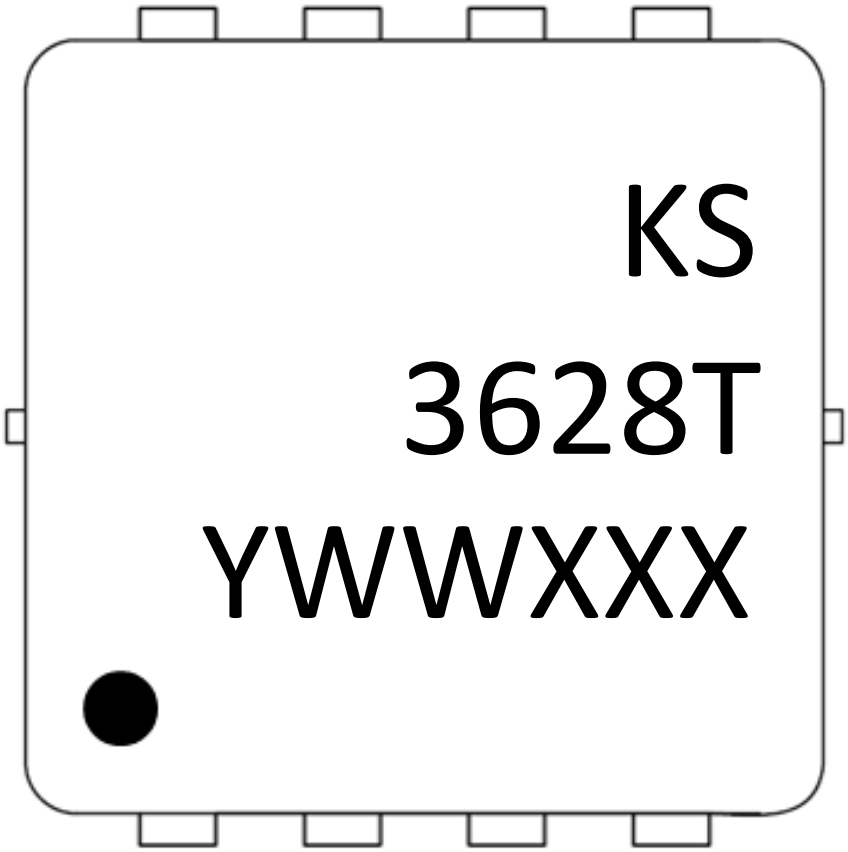
Symbol	Parameter	Test Condition		KS3628MAT			Unit
				Min.	Typ.	Max.	
Dynamic Characteristics⑥							
$t_{d(ON)}$	Turn-on Delay Time	N-Channel V_{DD}=15V, I_{DS}=12A, V_{GEN}=10V, R_G=3Ω P-Channel V_{DD}=-15V, I_{DS}=-12A, V_{GEN}= -10V, R_G=3Ω	N		8		ns
			P		13		
t_r	Turn-on Rise Time		N		10		
			P		25		
$t_{d(OFF)}$	Turn-off Delay Time		N		12		
			P		38		
t_f	Turn-off Fall Time		N		5		
			P		14		
Gate Charge Characteristics⑥							
Q_g	Total Gate Charge	N-Channel V_{DS}=15V, V_{GS}=10V, I_{DS}=12A P-Channel V_{DS}=-15V, V_{GS}= -10V, I_{DS}=-12A	N		4.8		nC
			P		21		
Q_{gs}	Gate-Source Charge		N		2.3		
			P		6		
Q_{gd}	Gate-Drain Charge		N		1.4		
			P		5		

Notes:

①Pulse width limited by safe operating area.
②Calculated continuous current based on maximum allowable junction temperature.
③When mounted on 1 inch square copper board, t≤10sec. The value in any given application depends on the user's specific board design.
④Limited by $T_{Jmax.}$ Starting T_J = 25°C, N Chanel: L = 0.5mH,R_G = 25Ω, I_{AS} = 7A, V_{GS} =10V, P-Chanel: L = 0.5mH, R_G = 25Ω, I_{AS} = -8A, V_{GS} =-10V, 100% tested and guaranteed.
⑤Pulse test;Pulse width≤300μs, duty cycle≤2%.
⑥Guaranteed by design, not subject to production testing.

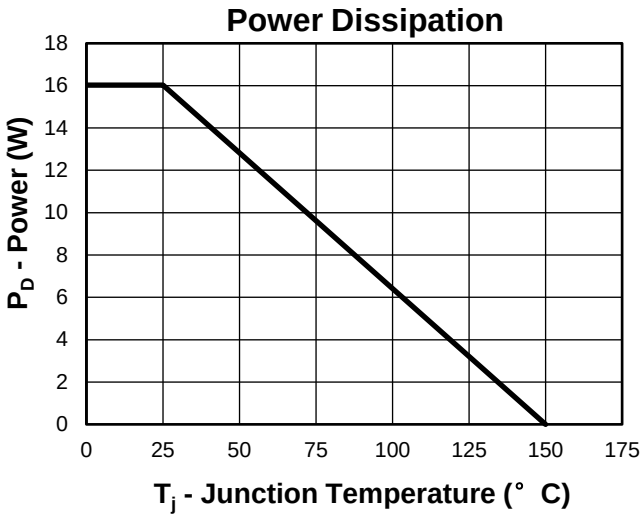
Ordering and Marking Information

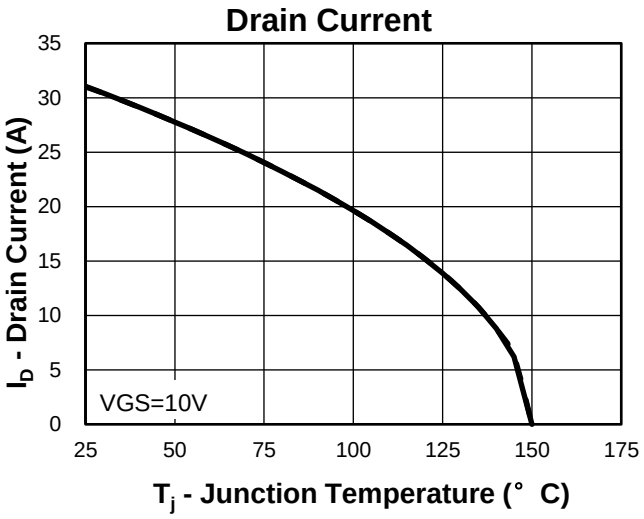
Device	Package	Packaging	Quantity	Reel Size	Tape width
KS3628MAT	PDFN3333 DP1	Tape&Reel	5000	13"	12mm

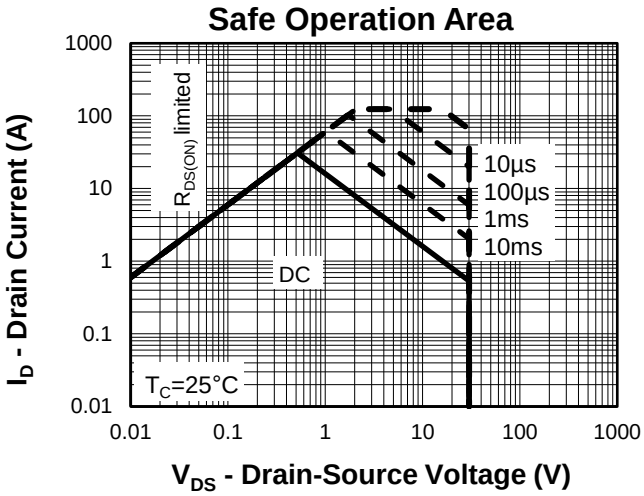


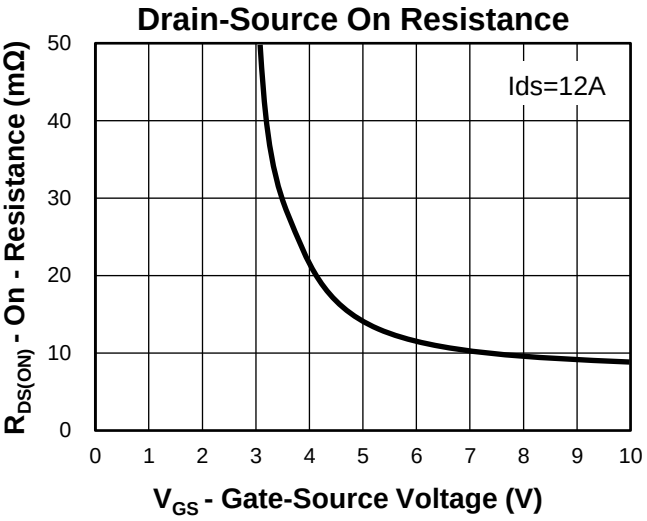
1st Line: Kwansemi Code(KS)
2nd Line: Part Number(3628T)
3rd Line: Lot Number(YWWXXX)

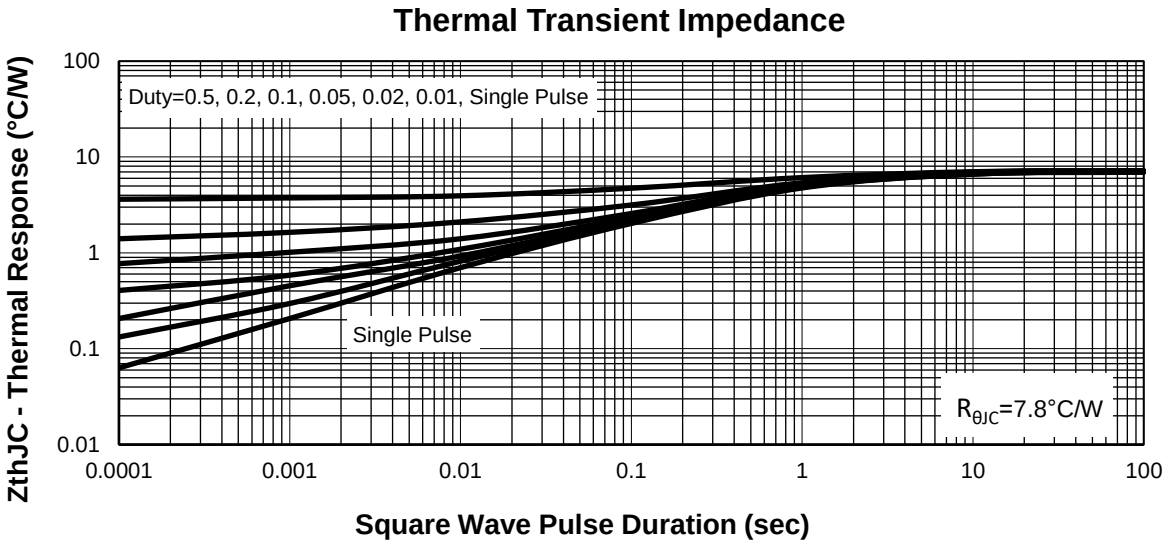
Typical Characteristics(N-Channel)



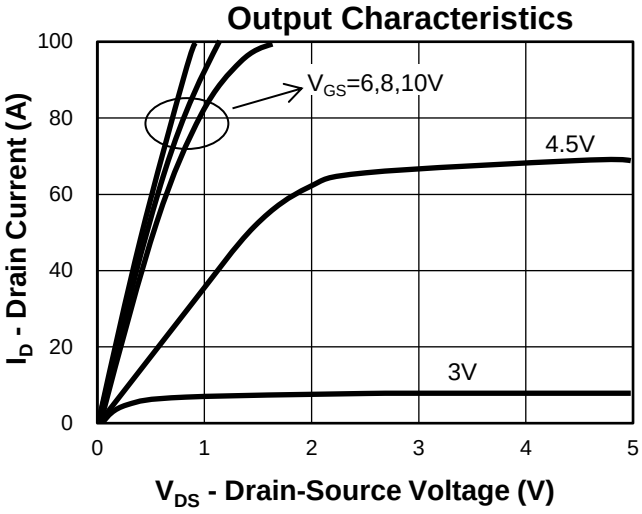


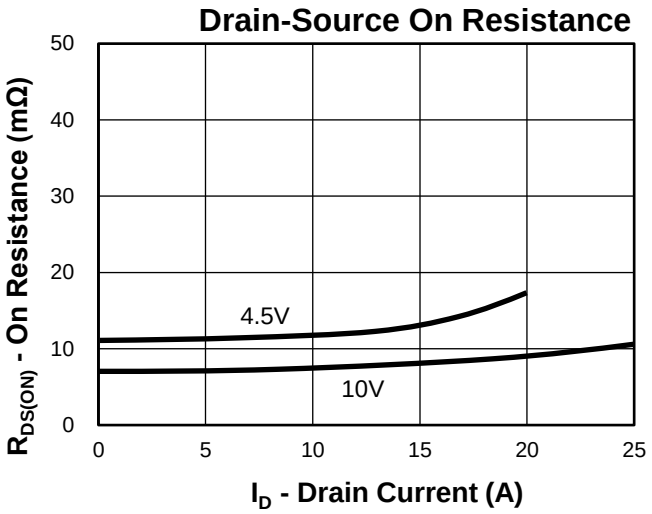


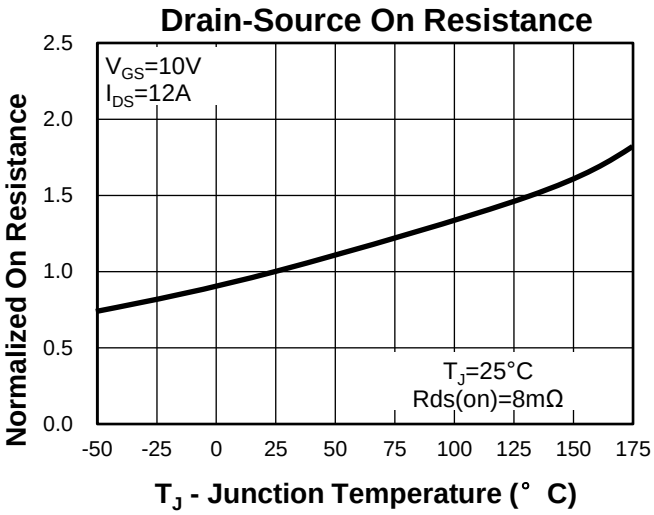


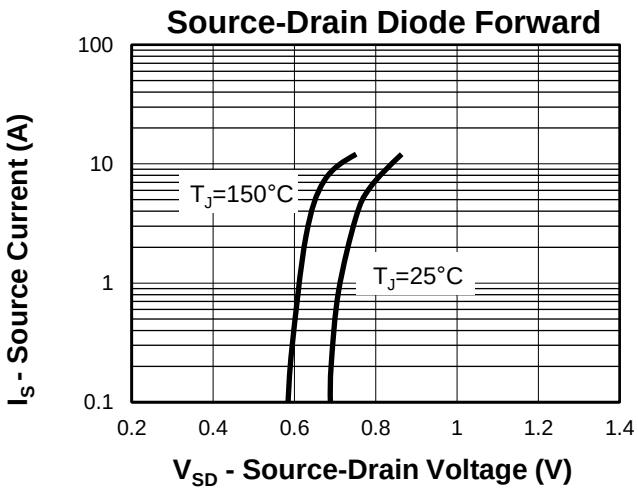


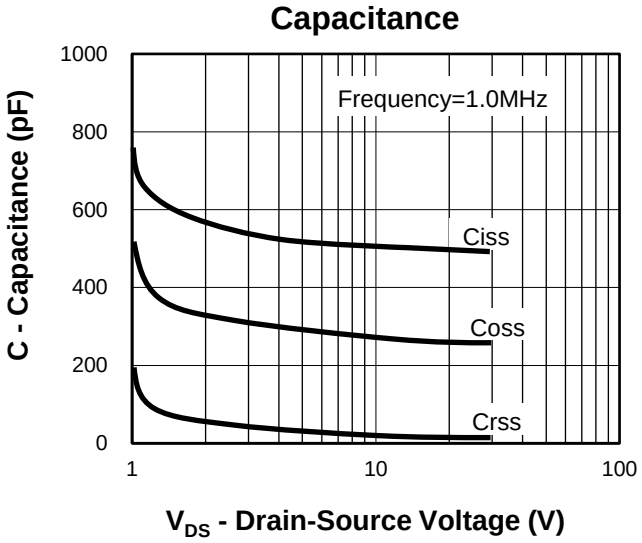
Typical Characteristics(N-Channel)

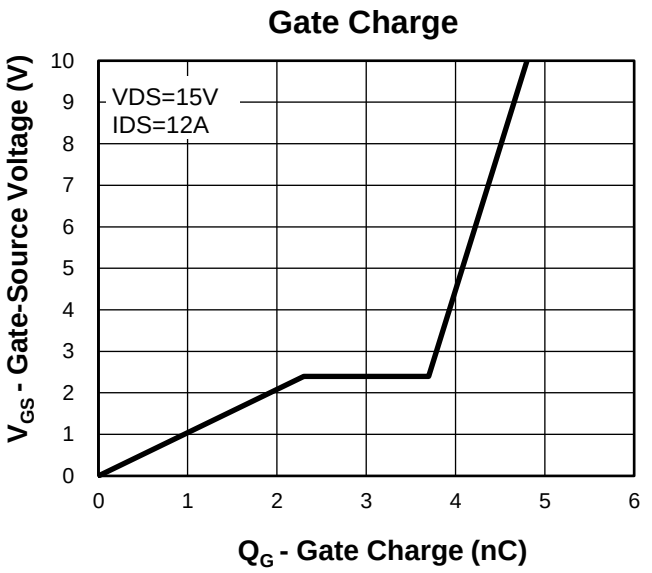




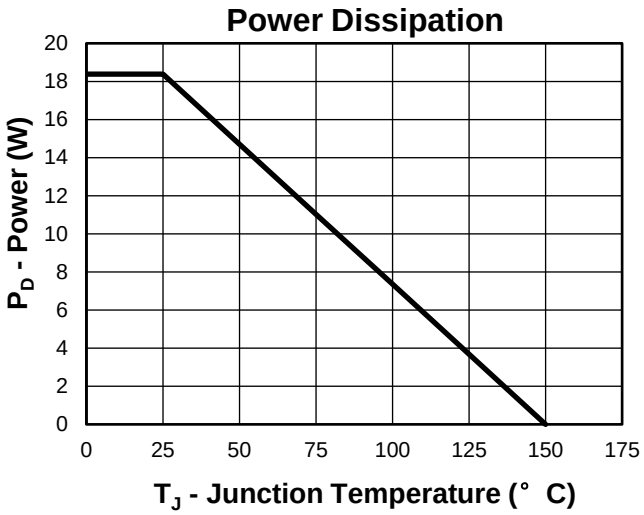


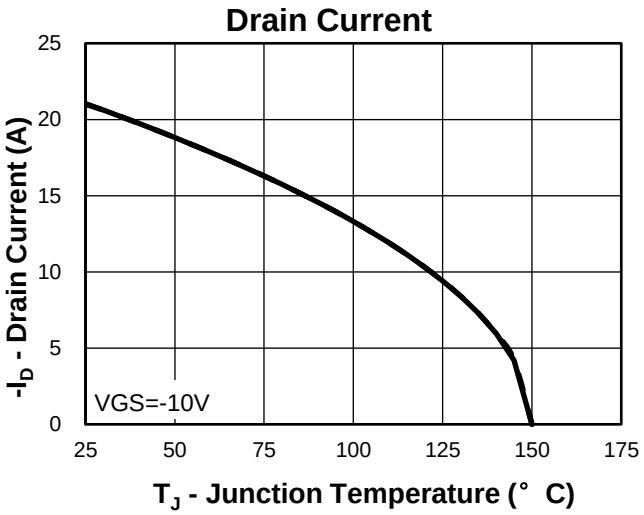


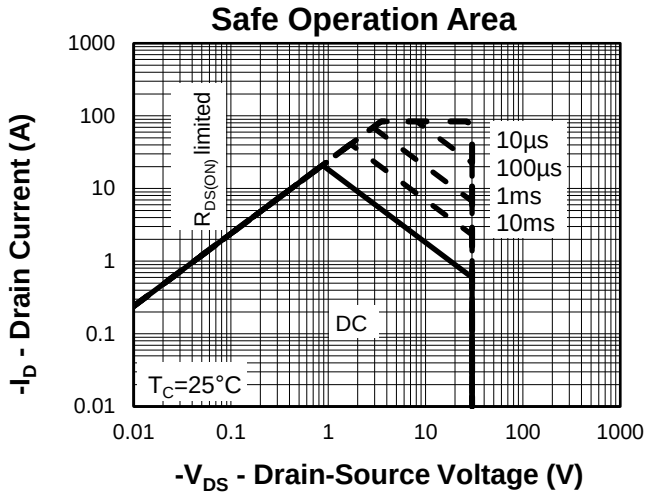


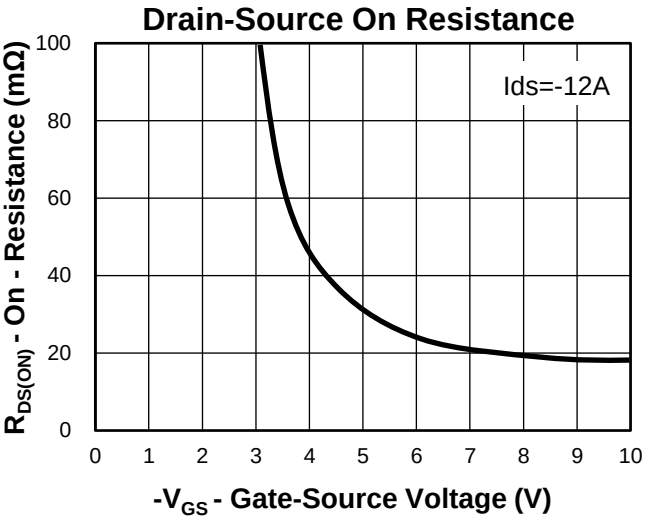


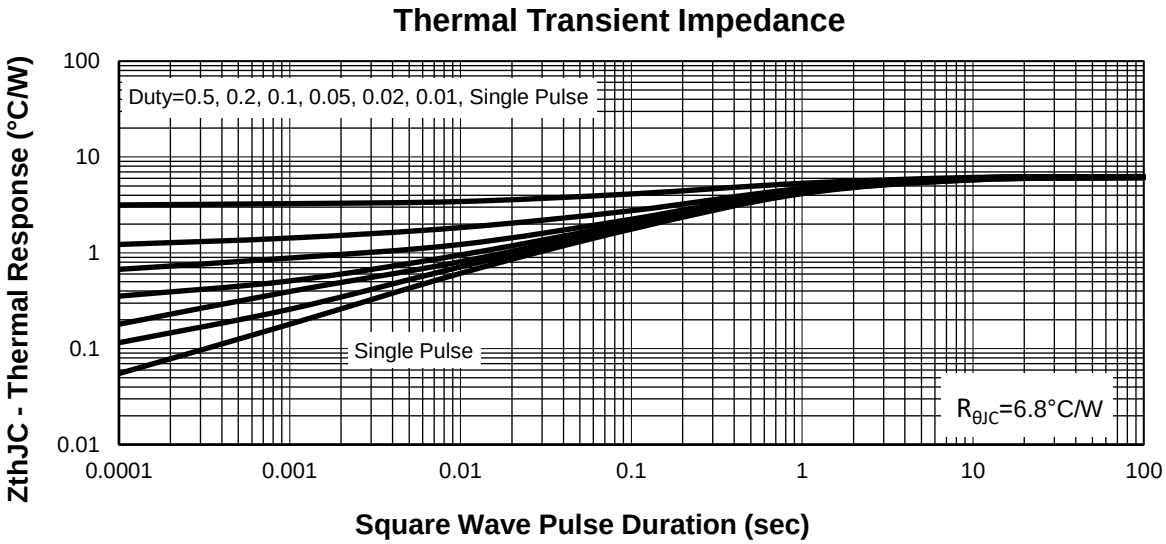
Typical Characteristics(P-Channel)



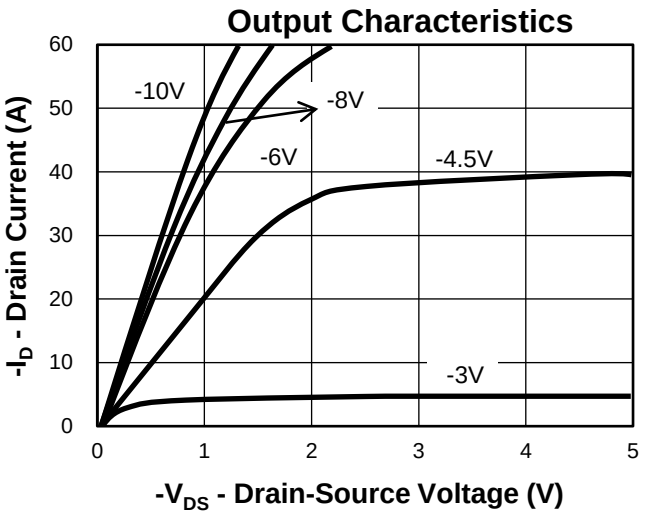


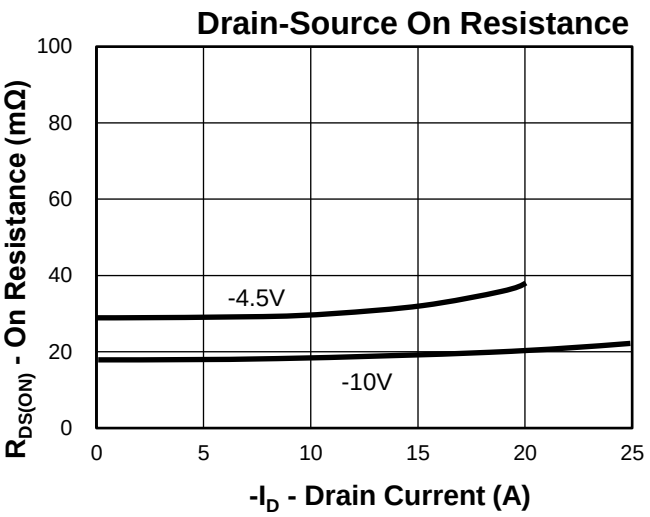


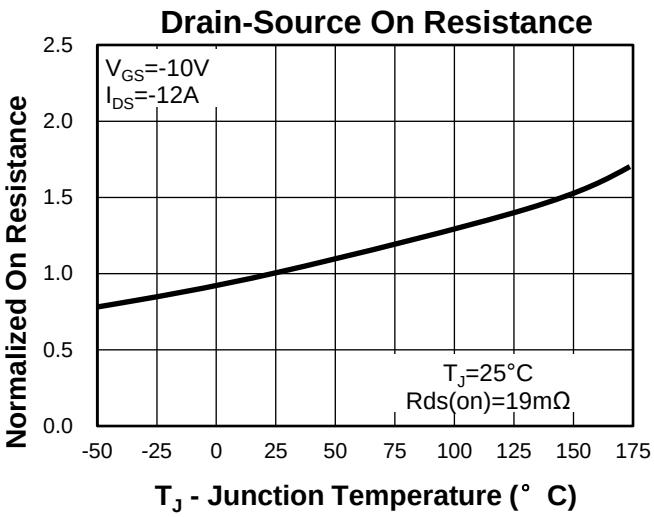


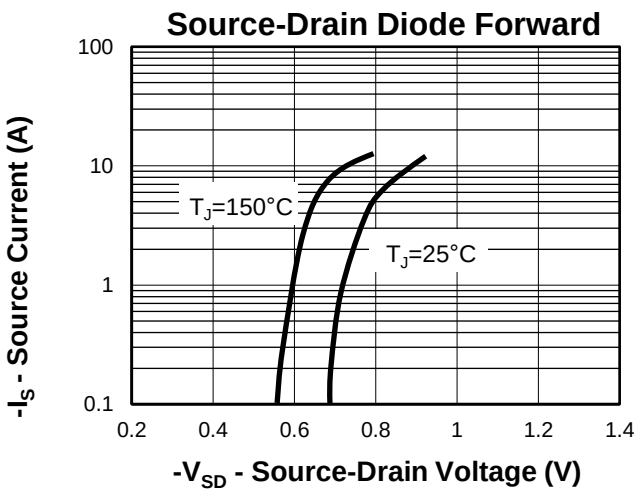


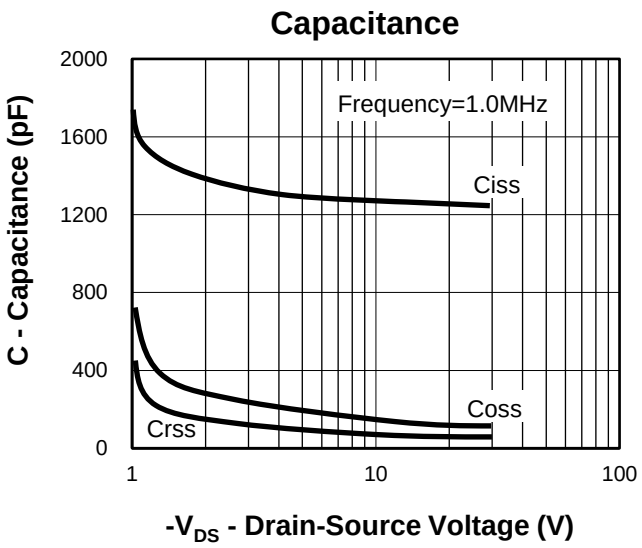
Typical Characteristics(P-Channel)

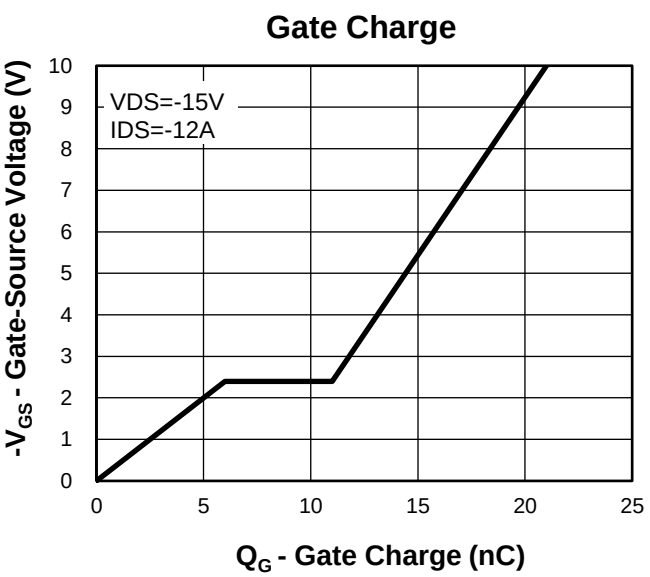






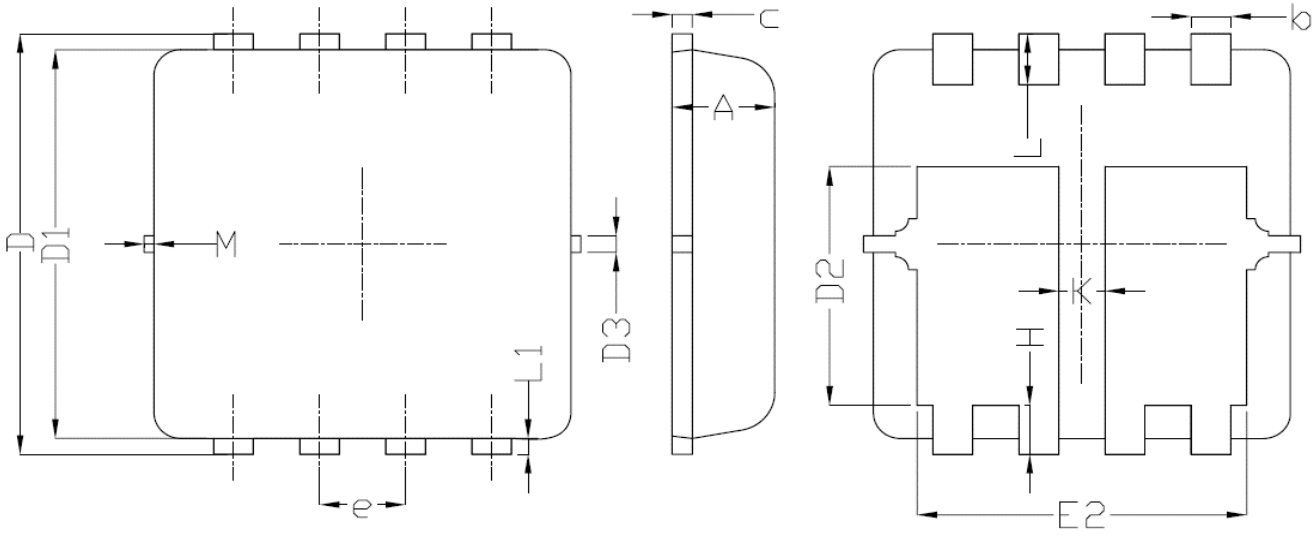


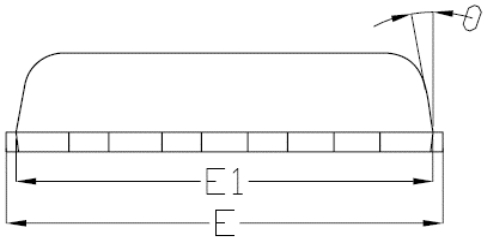




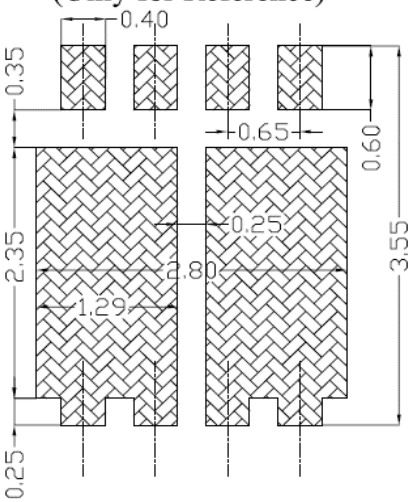
Package Information

PDFN3333 DP1



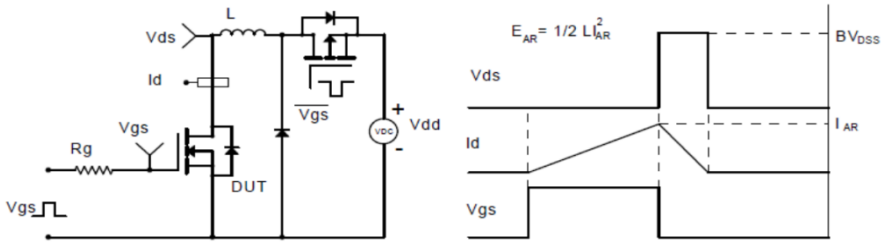


Land Pattern
(Only for Reference)

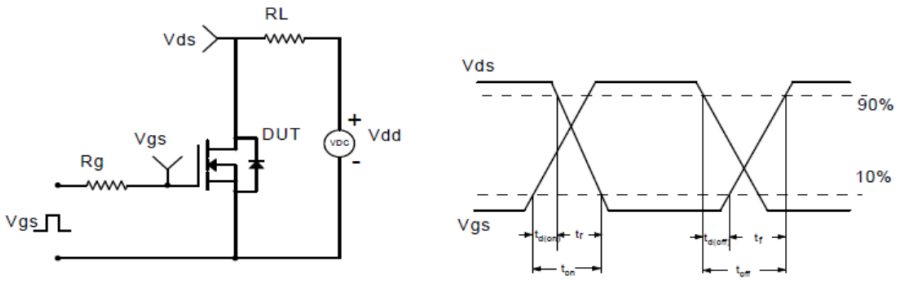


SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.70	0.75	0.80	0.028	0.030	0.031
b	0.25	0.30	0.35	0.010	0.012	0.014
c	0.10	0.15	0.25	0.004	0.006	0.010
D	3.25	3.35	3.45	0.128	0.132	0.136
D1	3.00	3.10	3.20	0.118	0.122	0.126
D2	1.78	1.88	1.98	0.070	0.074	0.078
D3		0.13			0.005	
E	3.20	3.30	3.40	0.126	0.130	0.134
E1	3.00	3.15	3.20	0.118	0.124	0.126
E2	2.39	2.49	2.59	0.094	0.098	0.102
e	0.65 BSC			0.026 BSC		
H	0.30	0.39	0.50	0.012	0.015	0.020
L	0.30	0.40	0.50	0.012	0.016	0.020
L1		0.13			0.005	
K	0.30			0.012		
θ	0°		12°	0°		12°
M			0.15			0.006

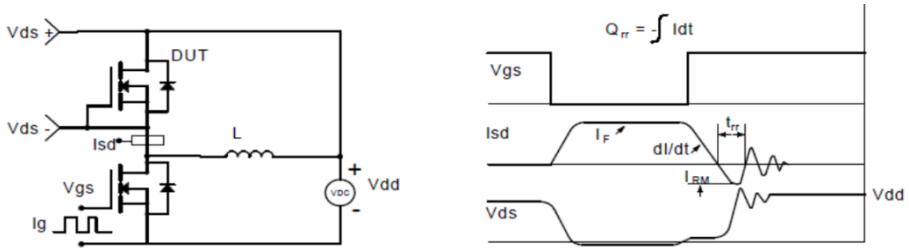
Avalanche Test Circuit and Waveforms(N-Channel)



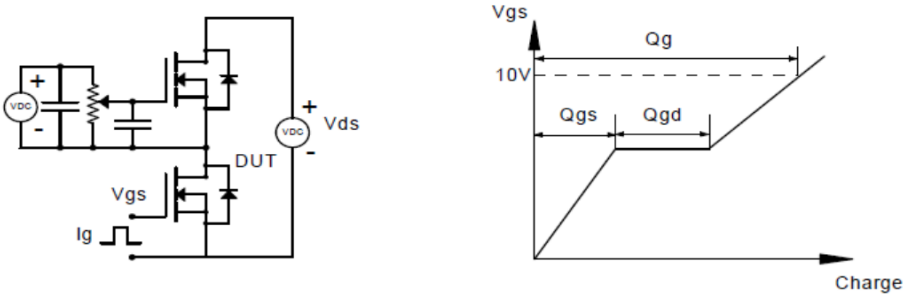
Switching Time Test Circuit and Waveforms(N-Channel)



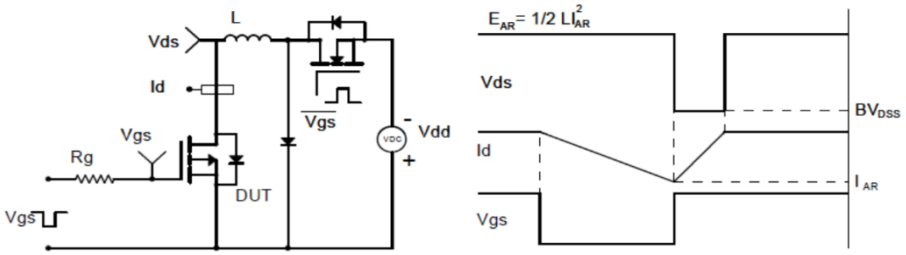
Diode Recovery Test Circuit and Waveforms(N-Channel)



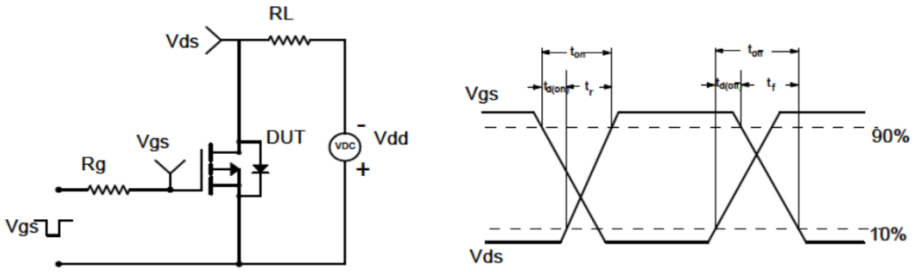
Gate Charge Test Circuit and Waveform(N-Channel)



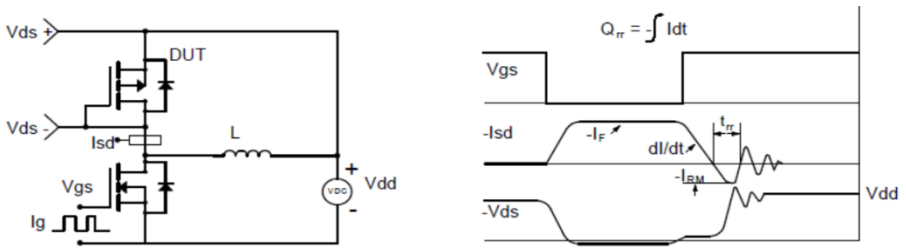
Avalanche Test Circuit and Waveforms(P-Channel)



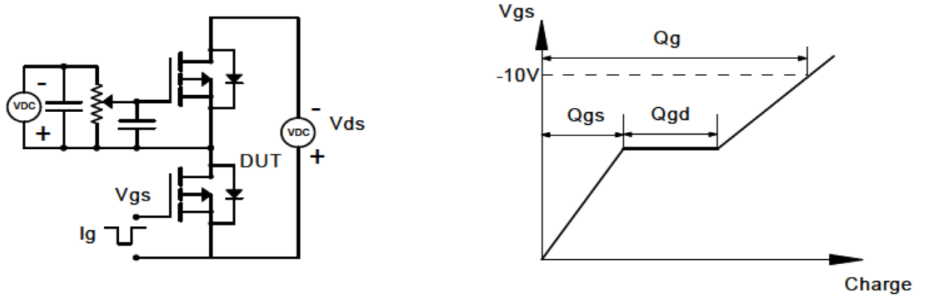
Switching Time Test Circuit and Waveforms(P-Channel)



Diode Recovery Test Circuit and Waveforms(P-Channel)



Gate Charge Test Circuit and Waveform(P-Channel)



Customer Service

Kwansemi Semiconductor Co.,Ltd

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Web:www.kwansemi.com

DISCLAIMER:

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