

Features

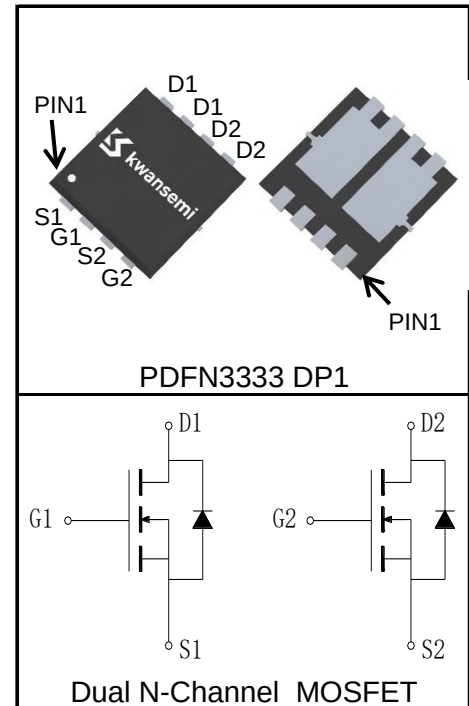
- 40V/26A,
 $R_{DS(ON)} = 14m\Omega(Typ.)@V_{GS}=10V$
 $R_{DS(ON)} = 19m\Omega(Typ.)@V_{GS}=4.5V$
- Low $R_{DS(ON)}$
- Super High Dense Cell Design
- Fast Switching Speed
- 100% avalanche tested

Applications

- Switching Application Systems



Pin Description



Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (T _C =25°C Unless Otherwise Noted)				
V _{DSS}	Drain-Source Voltage		40	V
V _{GSS}	Gate-Source Voltage		±20	
T _{Jmax}	Maximum Junction Temperature		150	°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to 150	°C
I _S	Diode Continuous Forward Current	T _C =25°C	26	A
Mounted on Large Heat Sink				
I _{DP} ^①	Pulse Drain Current	T _C =25°C	104	A
I _D ^②	Continuous Drain Current@T _C (V _{GS} =10V)	T _C =25°C	26	A
		T _C =100°C	16	
	Continuous Drain Current@T _A (V _{GS} =10V) ^③	T _A =25°C	9	
		T _A =70°C	7	
P _D	Maximum Power Dissipation@T _C	T _C =25°C	20	W
		T _C =100°C	8	
	Maximum Power Dissipation@T _A ^③	T _A =25°C	2.8	
		T _A =70°C	1.8	

Symbol	Parameter	Rating	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	6.2	°C/W
$R_{\theta JA}^{③}$	Thermal Resistance-Junction to Ambient	45	°C/W
Drain-Source Avalanche Ratings			
$E_{AS}^{④}$	Avalanche Energy, Single Pulsed	42	mJ

Electrical Characteristics ($T_C=25^{\circ}\text{C}$ Unless Otherwise Noted)

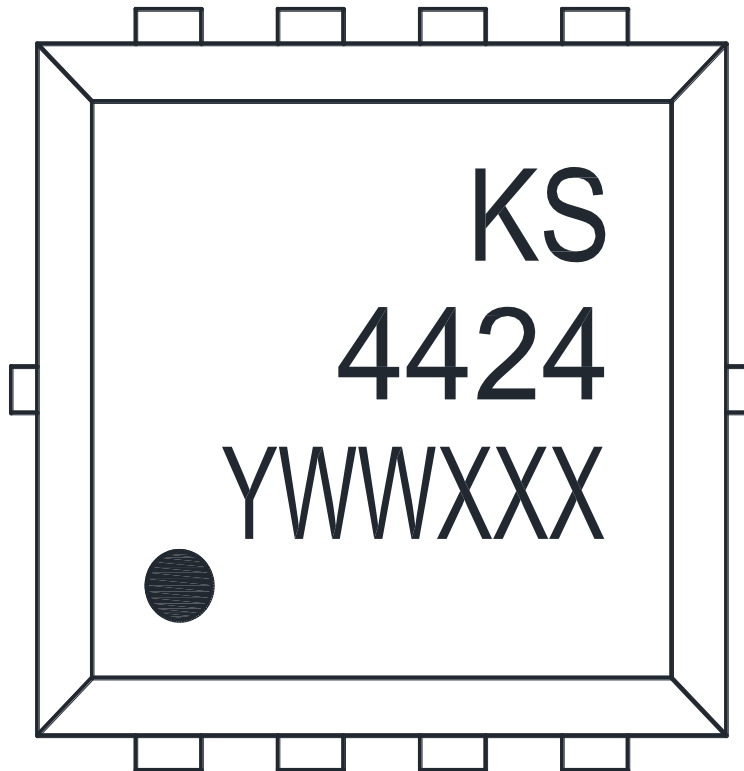
Symbol	Parameter	Test Condition	KS4424MA			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	40			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =40V, V _{GS} =0V			1	μA
		T _J =125°C			30	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	1.2	1.6	2.3	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
R _{DS(ON)} ⑤	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =12A		14	17	mΩ
		V _{GS} =4.5V, I _{DS} =8A		19	25	mΩ
Diode Characteristics						
V _{SD} ⑤	Diode Forward Voltage	I _{SD} =12A, V _{GS} =0V		0.87	1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} =12A, dI _{SD} /dt=100A/μs		15		ns
Q _{rr}	Reverse Recovery Charge			29		nC
Dynamic Characteristics ⑥						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz		4.7		Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =20V, Frequency=1.0MHz		945		pF
C _{oss}	Output Capacitance			75		
C _{rss}	Reverse Transfer Capacitance			65		
t _{d(ON)}	Turn-on Delay Time	V _{DD} =20V, I _{DS} =12A, V _{GS} =10V, R _G =3Ω		10		ns
t _r	Turn-on Rise Time			9		
t _{d(OFF)}	Turn-off Delay Time			11		
t _f	Turn-off Fall Time			8		
Gate Charge Characteristics ⑥						
Q _g	Total Gate Charge	V _{DS} =20V, V _{GS} =10V, I _{DS} =12A		19		nC
Q _{gs}	Gate-Source Charge			2.9		
Q _{gd}	Gate-Drain Charge			4.3		

Notes:

- ①Pulse width limited by safe operating area.
- ②Calculated continuous current based on maximum allowable junction temperature.
- ③When mounted on 1 inch square copper board, $t \leq 10\text{sec}$.
- ④Limited by $T_{J\text{max}}$, Starting $T_J = 25^\circ\text{C}$, $I_{AS\text{max}} = 13\text{A}$, $L = 0.5\text{mH}$, $V_{DD} = 20\text{V}$, $R_G = 25\Omega$, $V_{GS} = 10\text{V}$. Part not recommended for use above this value. 100% Final Test at $I_{AS} = 6\text{A}$, $L = 0.5\text{mH}$.
- ⑤Pulse test; Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
- ⑥Guaranteed by design, not subject to production testing.

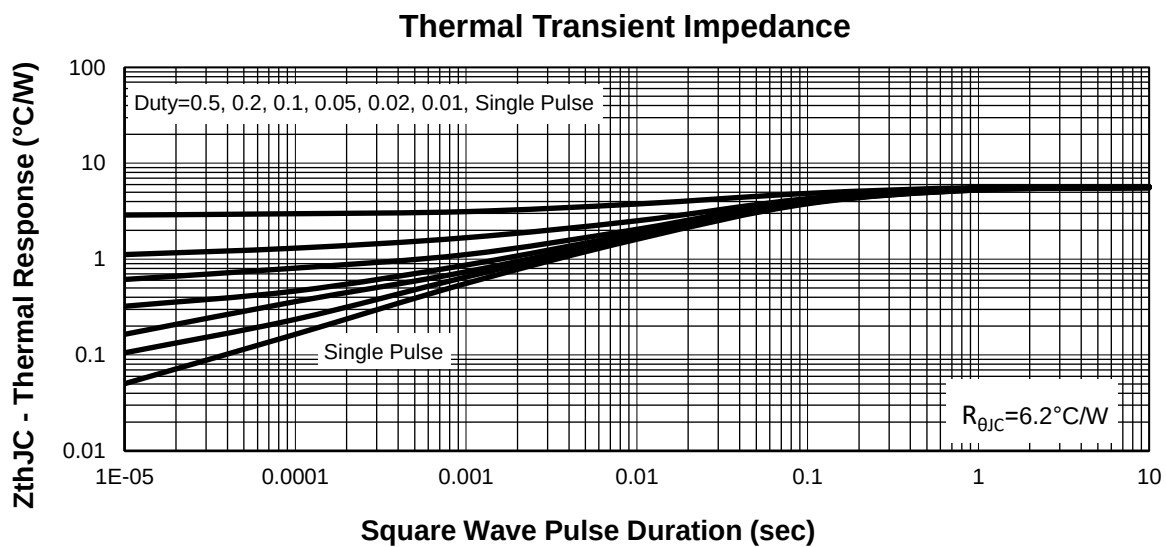
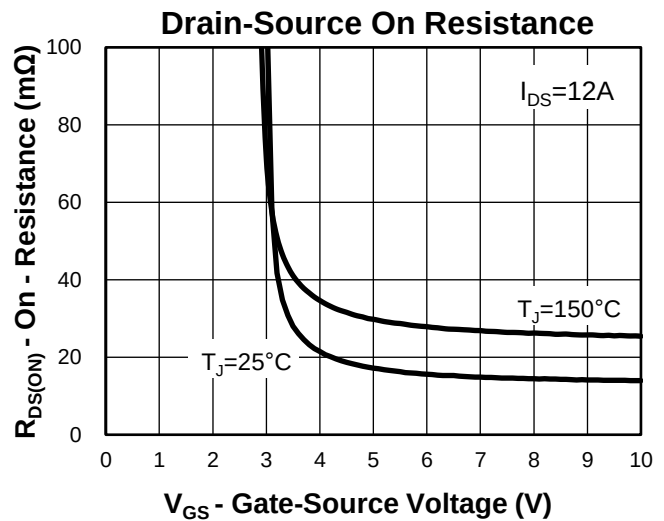
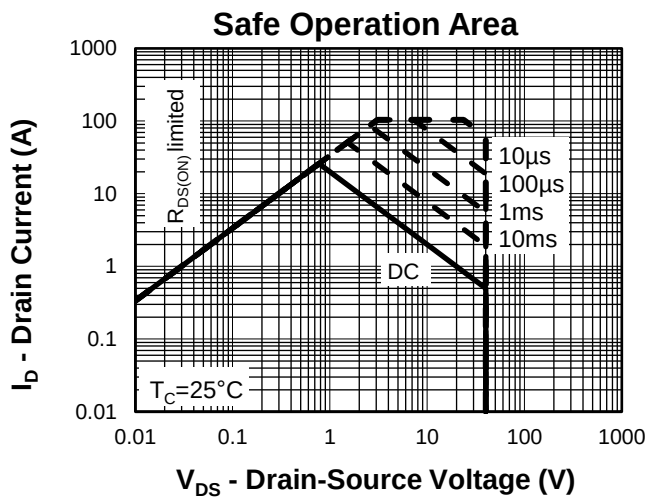
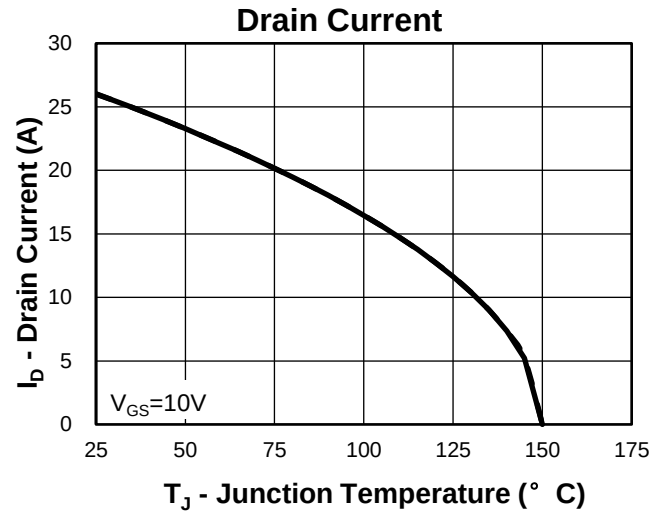
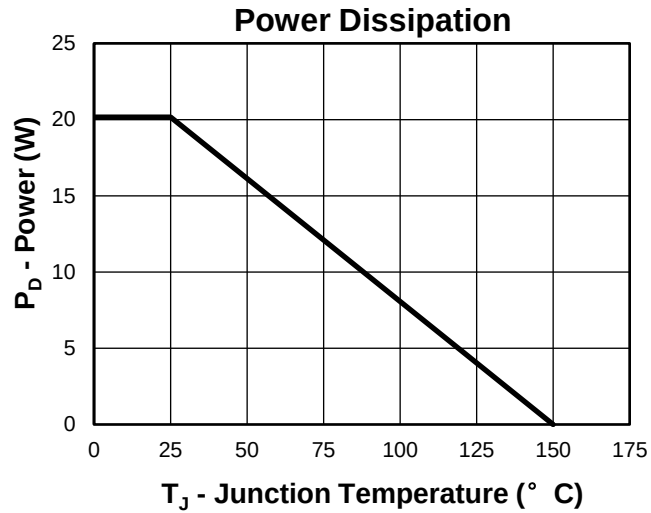
Ordering and Marking Information

Device	Package	Packaging	Quantity	Reel Size	Tape width
KS4424MA	PDFN3333 DP1	Tape&Reel	5000	13"	12mm

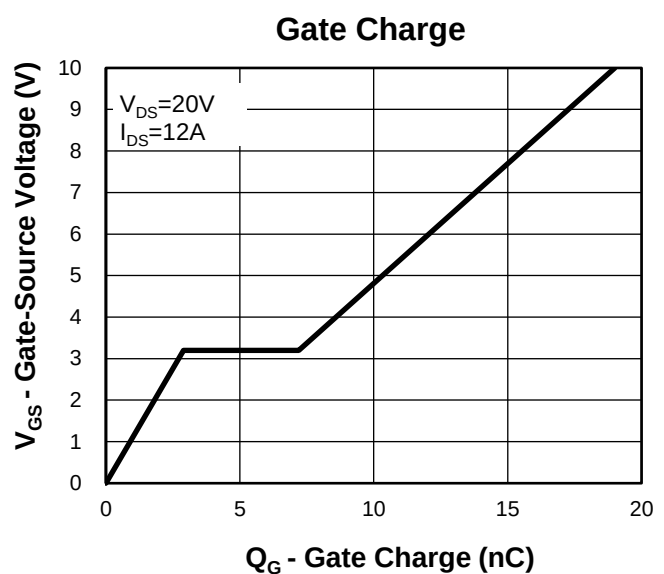
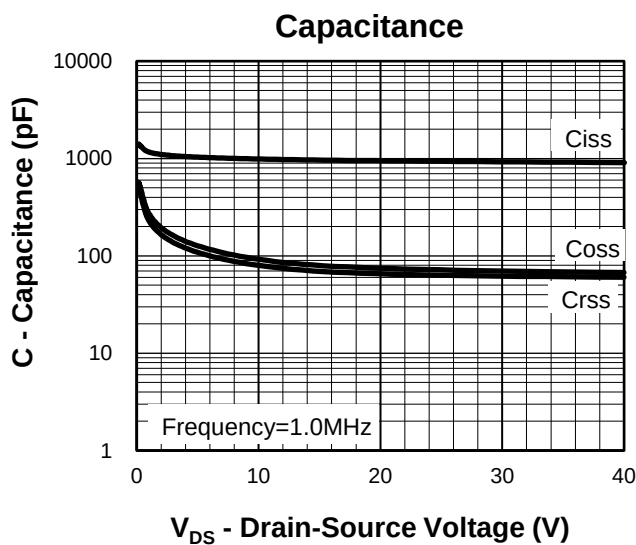
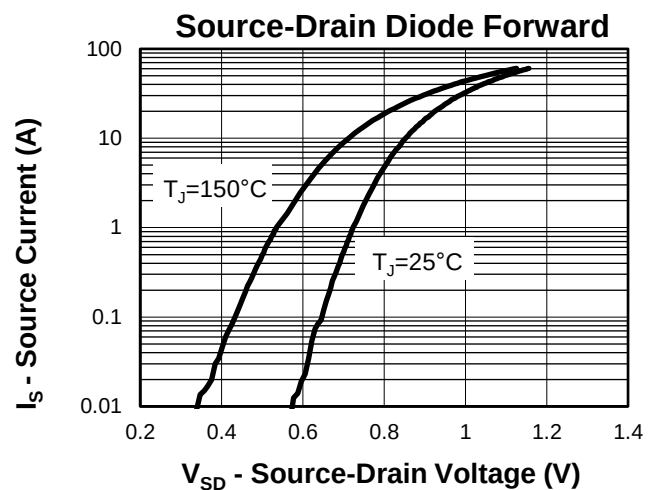
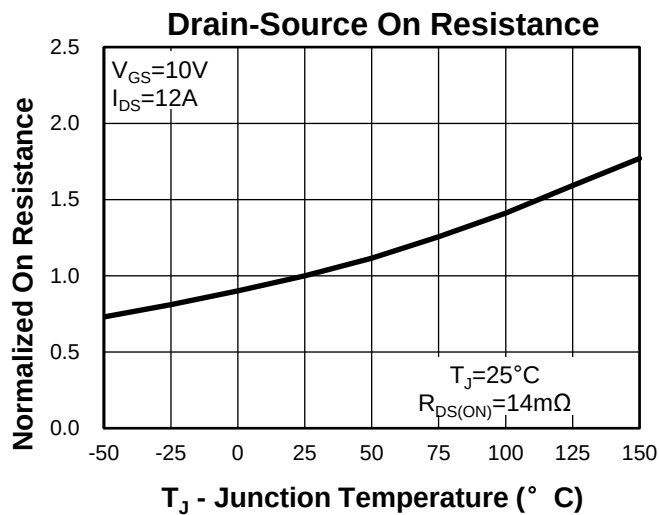
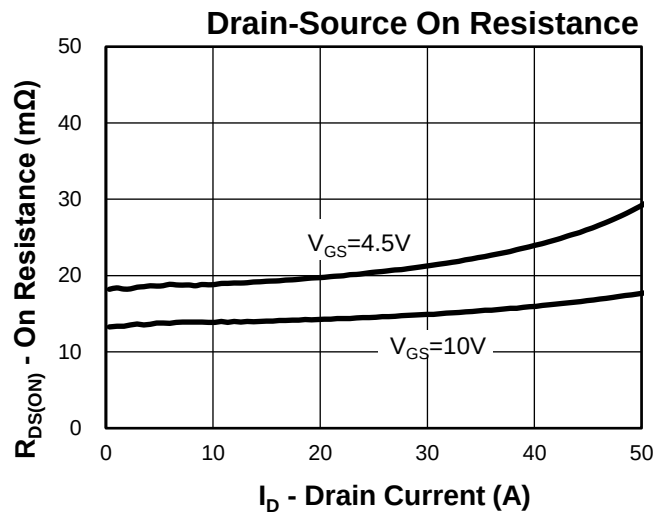
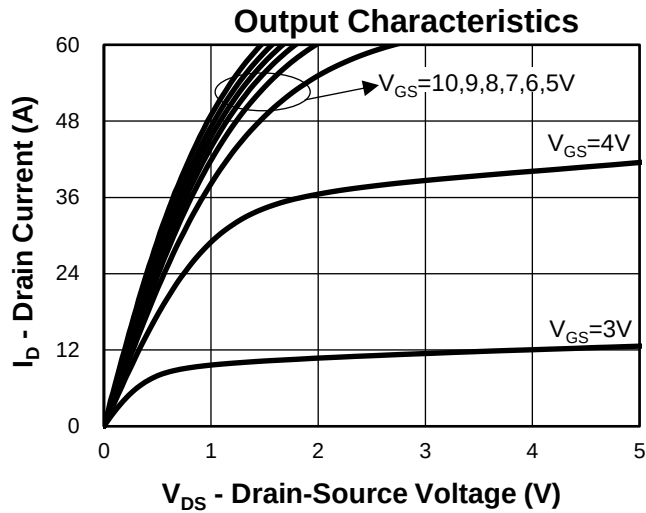


1st Line: Kwansemi Code(KS)
 2nd Line: Part Number(4424)
 3rd Line: Lot Number(YWWXXX)

Typical Characteristics

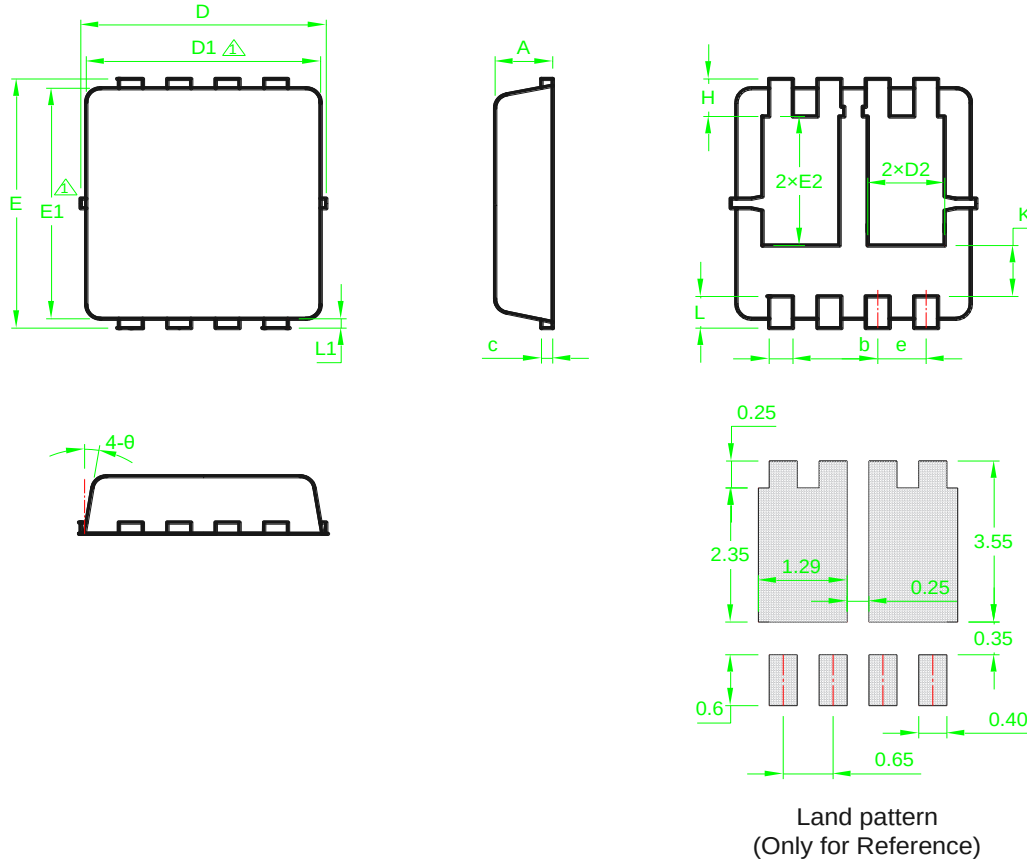


Typical Characteristics



Package Information

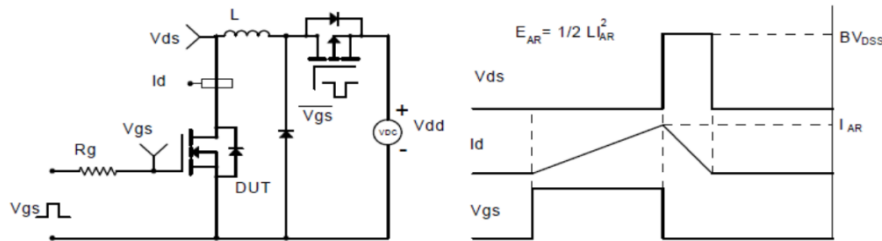
PDFN3333 DP1



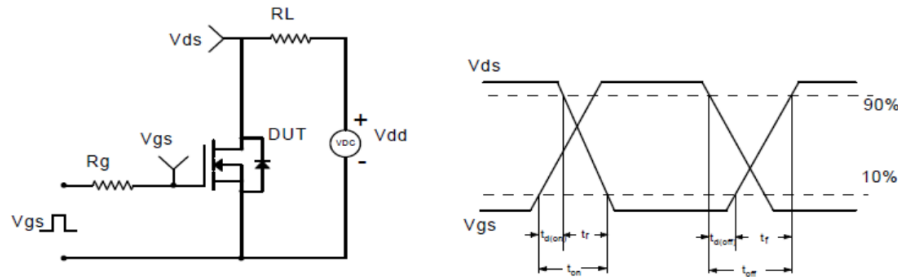
SYMBOL	MM			INCH			SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX		MIN	NOM	MAX	MIN	NOM	MAX
A	0.70	0.80	0.90	0.028	0.031	0.035	E2	1.65	*	1.95	0.065	*	0.077
b	0.25	0.30	0.35	0.010	0.012	0.014	e	0.65BSC			0.026BSC		
c	0.10	0.15	0.25	0.004	0.006	0.010	H	0.30	0.40	0.50	0.012	0.016	0.020
D	3.20	3.30	3.40	0.126	0.130	0.134	K	0.50	*	0.80	0.020	*	0.031
D1	3.00	3.15	3.25	0.118	0.124	0.128	L	0.30	0.40	0.50	0.012	0.016	0.020
D2	0.80	1.00	1.20	0.031	0.039	0.047	L1	0.10	0.15	0.20	0.004	0.006	0.008
E	3.20	3.30	3.40	0.126	0.130	0.134	θ	8°	*	12°	8°	*	12°
E1	2.90	3.05	3.20	0.114	0.120	0.126							

△ Dimensions D1 and E1 do not include mold flash protrusions or gate burrs.

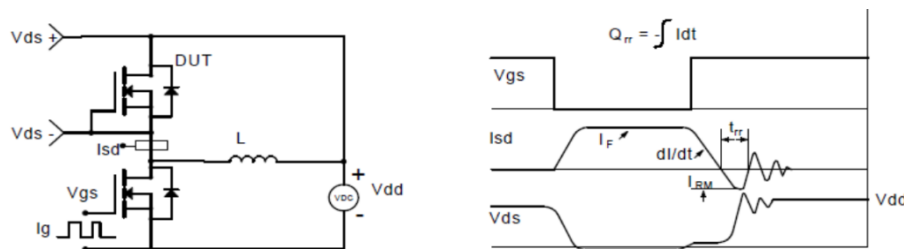
Avalanche Test Circuit and Waveforms



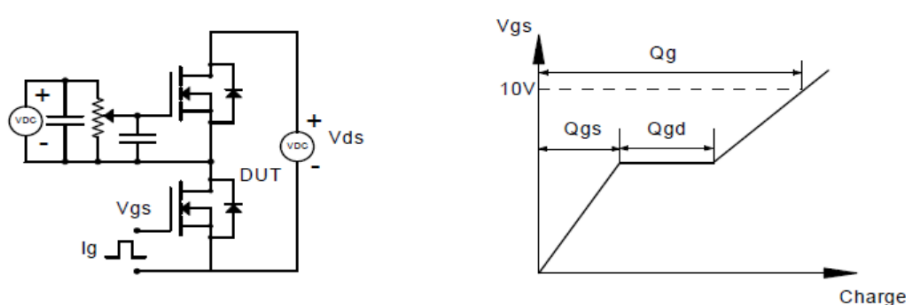
Switching Time Test Circuit and Waveforms



Diode Recovery Test Circuit and Waveforms



Gate Charge Test Circuit and Waveform



Customer Service

Kwansemi Semiconductor Co.,Ltd

Email:Sales@kwansemi.com

Web:www.kwansemi.com

DISCLAIMER:

Kwansemi reserves the right to change the specifications and circuitry without notice at any time. The Products are not designed for use in hostile environments, including, without limitation, aircraft, nuclear power generation, medical appliances, and devices or systems in which malfunction of any Product can reasonably be expected to result in a personal injury. Seller's customers using or selling Seller's products for use in such applications do so at their own risk and agree to fully defend and indemnify Seller.