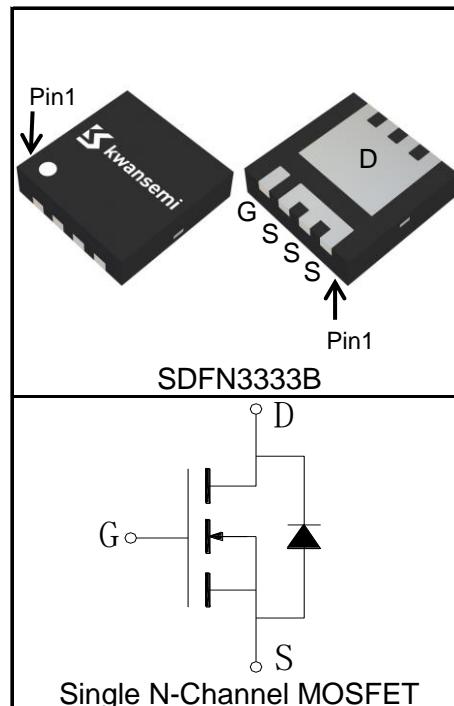


## Features

- 30V/106A,  
 $R_{DS(on)} = 1.6\text{m}\Omega(\text{Typ.}) @ V_{GS}=10\text{V}$   
 $R_{DS(on)} = 2.3\text{m}\Omega(\text{Typ.}) @ V_{GS}=4.5\text{V}$
- Excellent  $Q_G \times R_{DS(on)}$  product(FOM)
- SGT Technology
- High Ruggedness
- 100% Avalanche Tested

## Pin Description



## Applications

- Switching Application Systems



Halogen-Free

## Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
<b>Common Ratings</b> ( $T_C=25^\circ\text{C}$ Unless Otherwise Noted)			
$V_{DSS}$	Drain-Source Voltage	30	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	
$T_{Jmax}$	Maximum Junction Temperature	150	$^\circ\text{C}$
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$I_S$	Diode Continuous Forward Current	$T_C=25^\circ\text{C}$	106
			A

## Mounted on Large Heat Sink

$I_{DP}^{(1)}$	Pulse Drain Current	$T_C=25^\circ\text{C}$	424	A
$I_D^{(2)}$	Continuous Drain Current@ $T_C(V_{GS}=10\text{V})$	$T_C=25^\circ\text{C}$	106	A
		$T_C=100^\circ\text{C}$	67	
	Continuous Drain Current@ $T_A(V_{GS}=10\text{V})^{(3)}$	$T_A=25^\circ\text{C}$	33	
		$T_A=70^\circ\text{C}$	27	
$P_D$	Maximum Power Dissipation@ $T_C$	$T_C=25^\circ\text{C}$	34	W
		$T_C=100^\circ\text{C}$	13	
	Maximum Power Dissipation@ $T_A$ <sup>(3)</sup>	$T_A=25^\circ\text{C}$	3.5	
		$T_A=70^\circ\text{C}$	2.3	

Symbol	Parameter	Rating	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	3.6	°C/W
$R_{\theta JA}^{(3)}$	Thermal Resistance-Junction to Ambient	35	°C/W
<b>Drain-Source Avalanche Ratings</b>			
$E_{AS}^{(4)}$	Avalanche Energy, Single Pulsed	196	mJ

**Electrical Characteristics** ( $T_C=25^\circ C$  Unless Otherwise Noted)

Symbol	Parameter	Test Condition	KS3210UU3T			Unit
			Min.	Typ.	Max.	

**Static Characteristics**

$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=250\mu A$	30			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=30V, V_{GS}=0V$			1	$\mu A$
		$T_J=125^\circ C$			30	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=250\mu A$	1.1	1.6	2.3	V
$I_{GSS}$	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$			$\pm 100$	nA
$R_{DS(ON)}^{(5)}$	Drain-Source On-state Resistance	$V_{GS}=10V, I_{DS}=20A$		1.6	2.1	$m\Omega$
		$V_{GS}=4.5V, I_{DS}=15A$		2.3	3	$m\Omega$

**Diode Characteristics**

$V_{SD}^{(5)}$	Diode Forward Voltage	$I_{SD}=20A, V_{GS}=0V$		0.78	1.2	V
$t_{rr}$	Reverse Recovery Time	$I_{SD}=20A, dI_{SD}/dt=100A/\mu s$		17		ns
$Q_{rr}$	Reverse Recovery Charge			33		nC

**Dynamic Characteristics**<sup>(6)</sup>

$R_G$	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$		3.7		$\Omega$
$C_{iss}$	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=15V,$ Frequency=1.0MHz		1695		pF
$C_{oss}$	Output Capacitance			595		
$C_{rss}$	Reverse Transfer Capacitance			30		
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD}=15V, I_{DS}=20A,$ $V_{GS}=10V, R_G=3\Omega$		15		ns
$t_r$	Turn-on Rise Time			21		
$t_{d(OFF)}$	Turn-off Delay Time			39		
$t_f$	Turn-off Fall Time			15		

**Gate Charge Characteristics**<sup>(6)</sup>

$Q_g$	Total Gate Charge	$V_{DS}=15V, V_{GS}=10V,$ $I_{DS}=20A$		24		nC
$Q_{gs}$	Gate-Source Charge			4.2		
$Q_{gd}$	Gate-Drain Charge			2.5		

## Notes:

- ①Pulse width limited by safe operating area.
- ②Calculated continuous current based on maximum allowable junction temperature. The package limitation current is 40A.
- ③When mounted on 1 inch square copper board,  $t \leq 10\text{sec}$ .
- ④Limited by  $T_{J\max}$ , Starting  $T_J = 25^\circ\text{C}$ ,  $I_{AS\max} = 28\text{A}$ ,  $L = 0.5\text{mH}$ ,  $V_{DD} = 20\text{V}$ ,  $R_G = 25\Omega$ ,  $V_{GS} = 10\text{V}$ . Part not recommended for use above this value. 100% Final Test at  $I_{AS} = 14\text{A}$ ,  $L = 0.5\text{mH}$ .
- ⑤Pulse test; Pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- ⑥Guaranteed by design, not subject to production testing.

**Ordering and Marking Information**

Device	Package	Packaging	Quantity	Reel Size	Tape width
KS3210UU3T	SDFN3333B	Tape&Reel	5000	13"	12mm

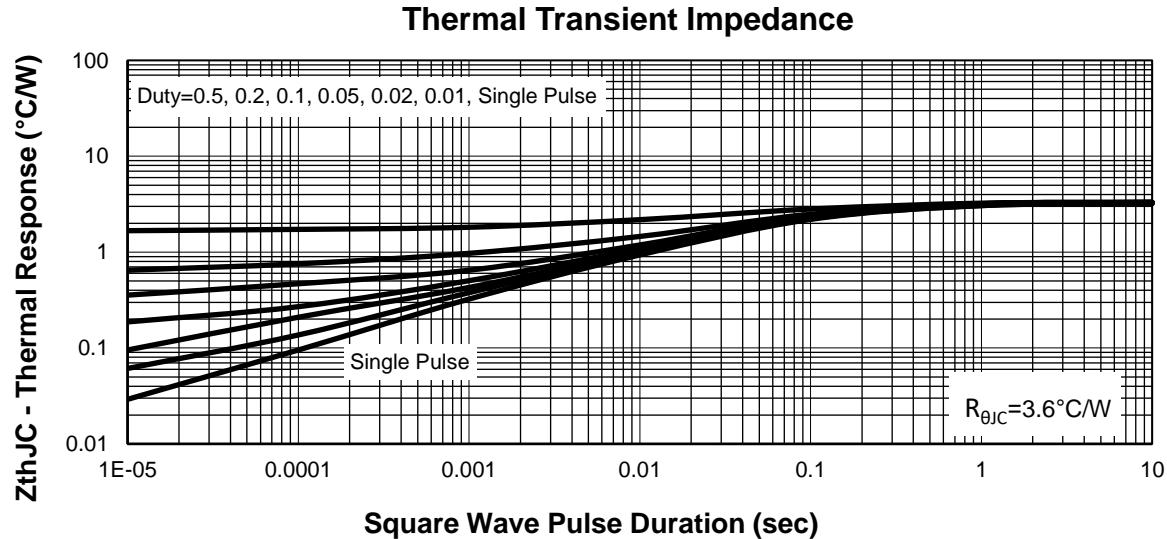
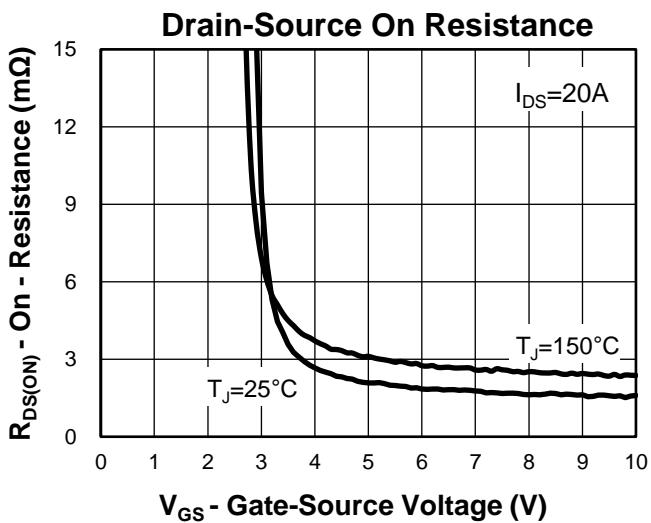
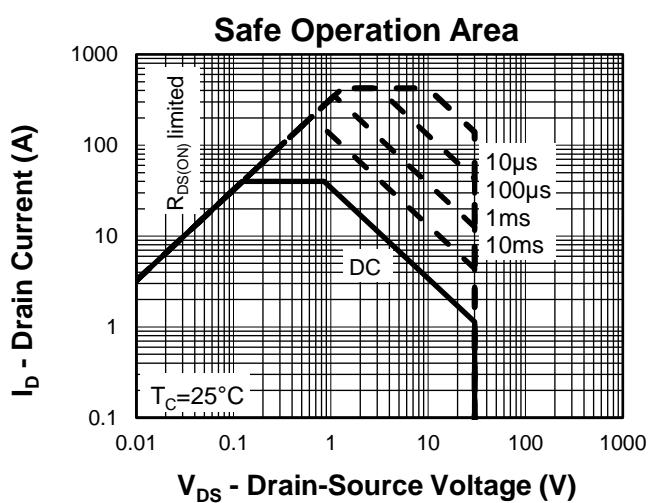
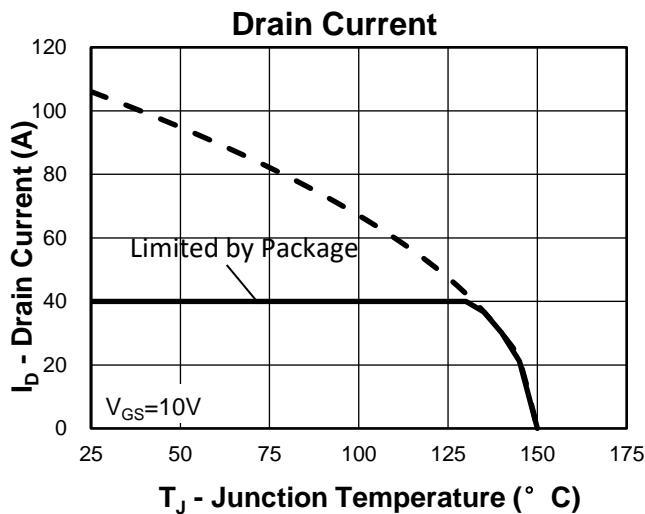
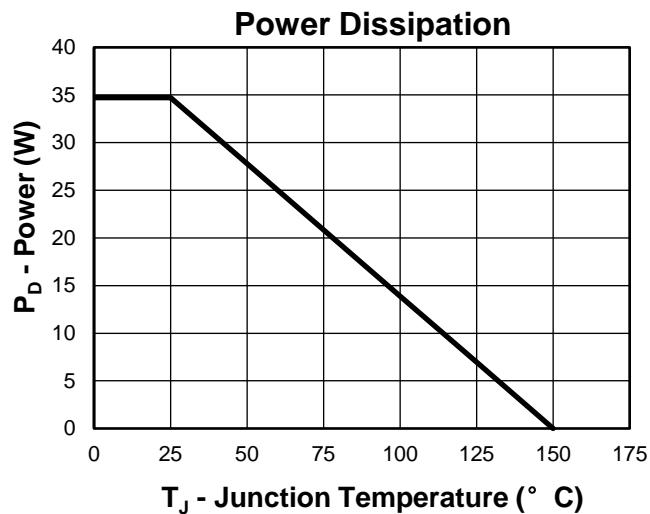


1st Line: Kwansemi Code(KS)

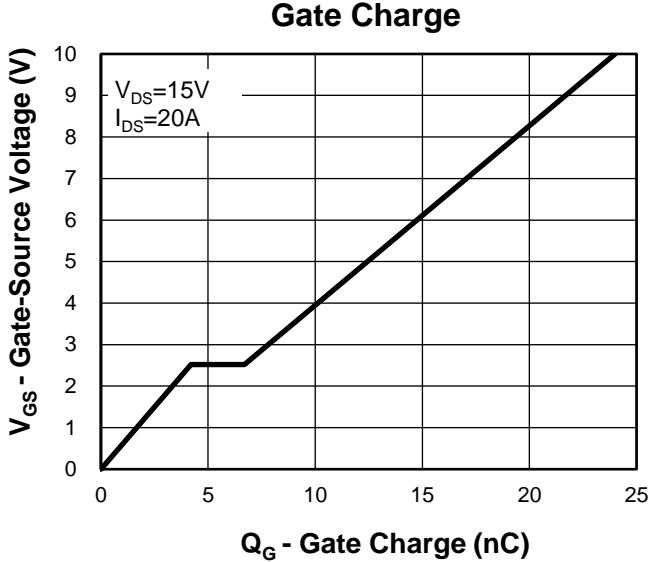
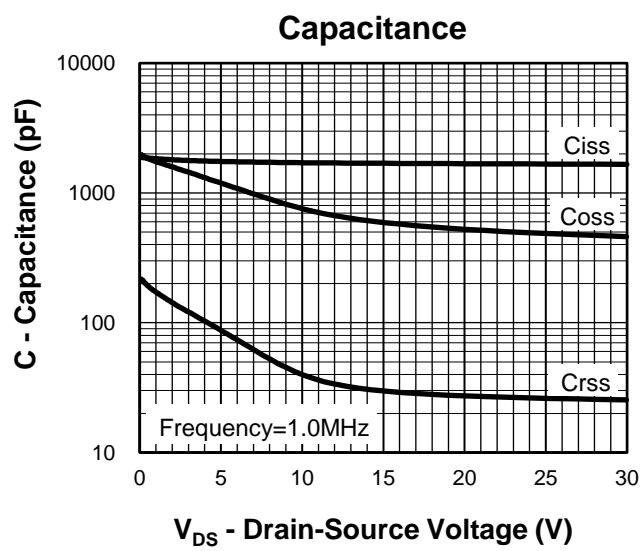
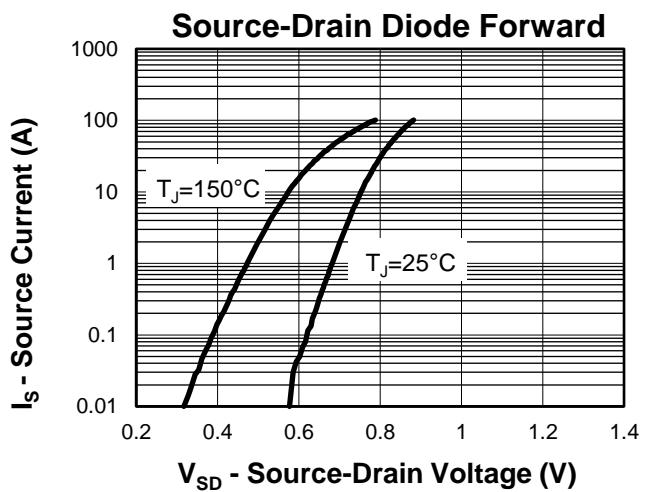
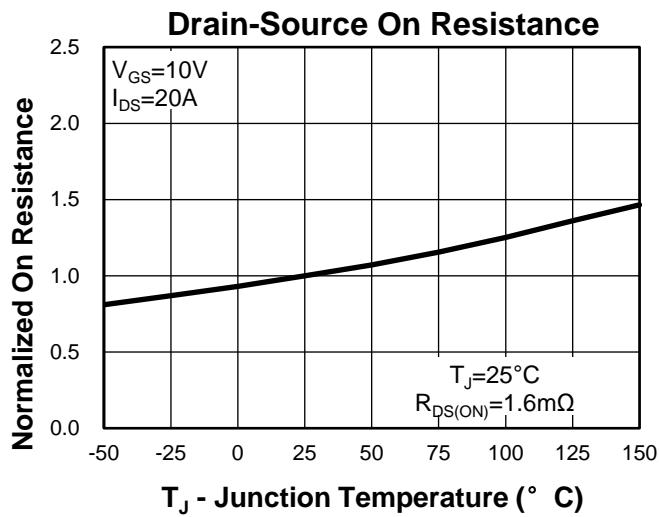
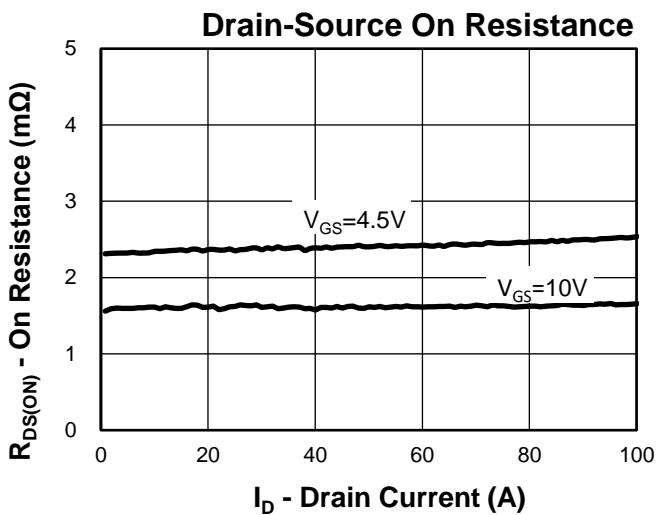
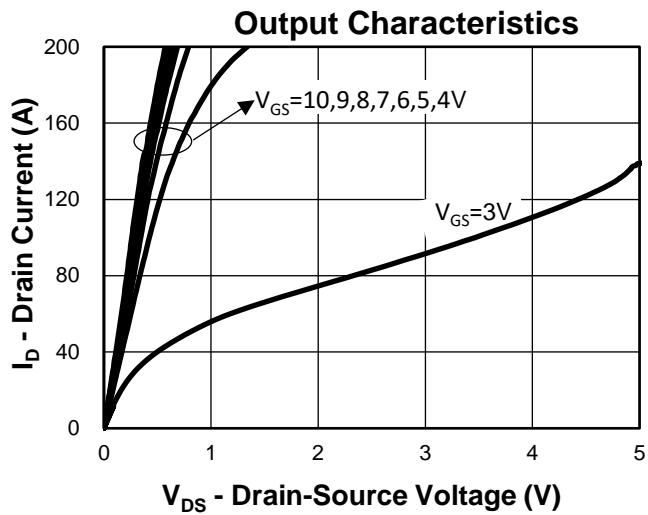
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3rd Line: Lot Number(YWWXXX)

## Typical Characteristics

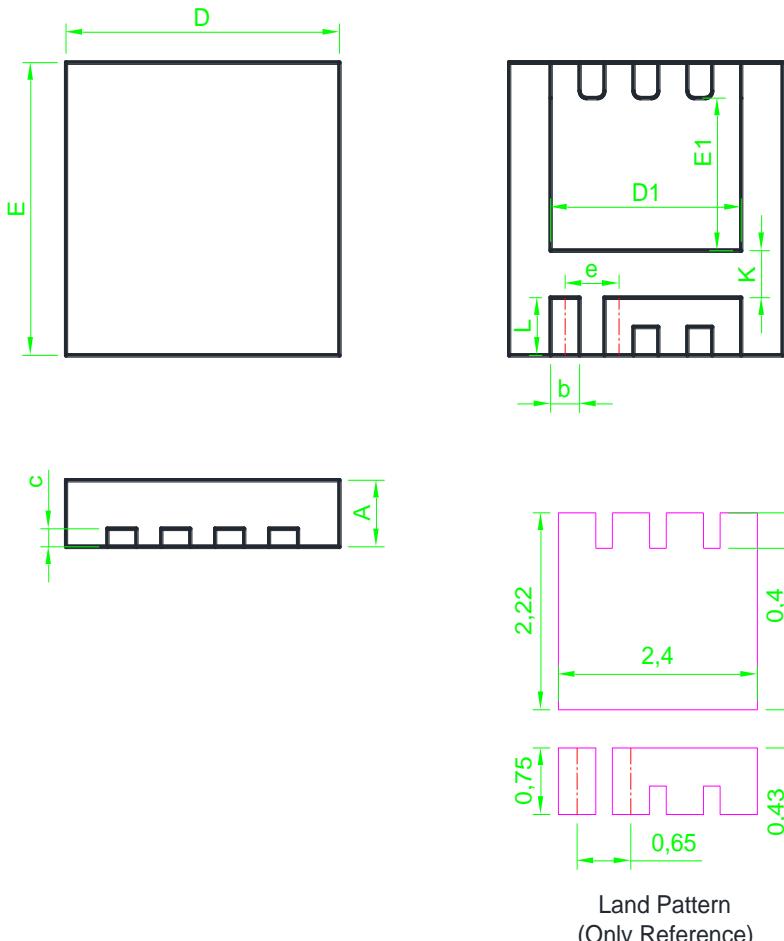


## Typical Characteristics



## Package Information

### SDFN3333B



Land Pattern  
(Only Reference)

SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.70	0.75	0.80	0.028	0.030	0.031
b	0.30	0.35	0.40	0.012	0.014	0.016
c	0.15	0.20	0.25	0.006	0.008	0.010
D	3.20	3.30	3.40	0.126	0.130	0.134
D1	2.20	2.30	2.40	0.087	0.091	0.094
e	0.60	0.65	0.70	0.024	0.026	0.028
E	3.20	3.30	3.40	0.126	0.130	0.134
E1	1.62	1.72	1.82	0.064	0.068	0.072
L	0.55	0.65	0.75	0.022	0.026	0.030
K	0.43	0.53	0.63	0.017	0.021	0.025

Note: Dimensions do not include burrs and mold flash.

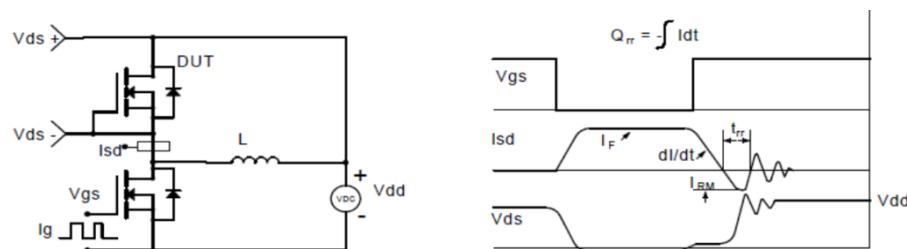
### Avalanche Test Circuit and Waveforms



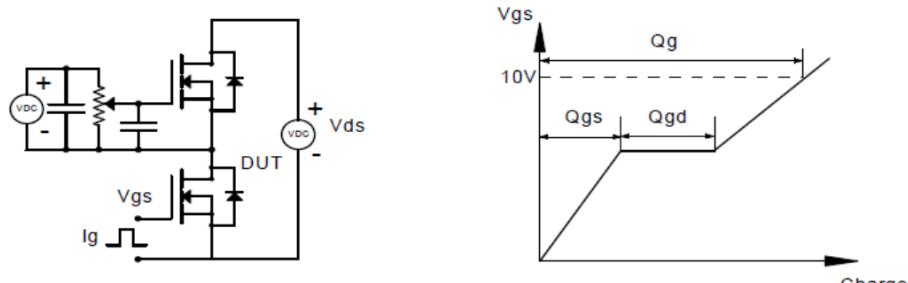
### Switching Time Test Circuit and Waveforms



### Diode Recovery Test Circuit and Waveforms



### Gate Charge Test Circuit and Waveform



### Customer Service

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