

## Features

- -30V/-71A,  
 $R_{DS(ON)} = 6.6m\Omega(Typ.)@V_{GS}=-10V$   
 $R_{DS(ON)} = 9m\Omega(Typ.)@V_{GS}=-4.5V$
- Low  $R_{DS(ON)}$
- Super High Dense Cell Design
- Reliable and Rugged
- 100% Avalanche Tested

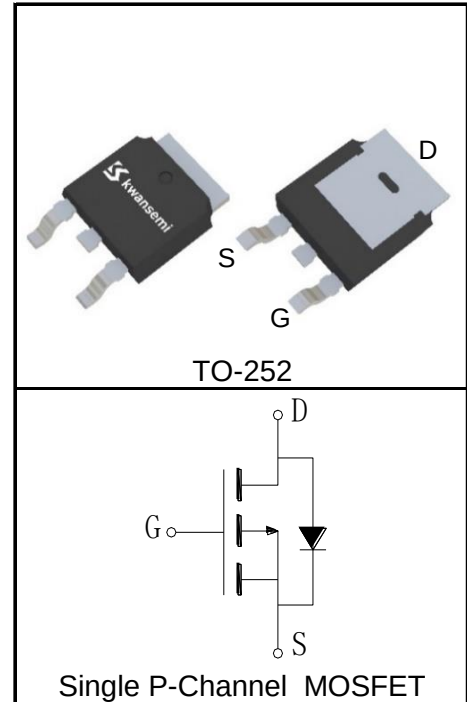
## Applications

- Power Switching Appliaction
- Load Switching



Halogen-Free

## Pin Description



## Absolute Maximum Ratings

| Symbol                                                            | Parameter                                 | Rating                    | Unit          |
|-------------------------------------------------------------------|-------------------------------------------|---------------------------|---------------|
| <b>Common Ratings</b> ( $T_C=25^{\circ}C$ Unless Otherwise Noted) |                                           |                           |               |
| $V_{DSS}$                                                         | Drain-Source Voltage                      | -30                       | V             |
| $V_{GSS}$                                                         | Gate-Source Voltage                       | $\pm 25$                  | V             |
| $T_{Jmax}$                                                        | Maximum Junction Temperature              | 175                       | $^{\circ}C$   |
| $T_J, T_{STG}$                                                    | Operating and Storage Temperature Range   | -55 to 175                | $^{\circ}C$   |
| $I_S$                                                             | Diode Continuous Forward Current          | $T_C=25^{\circ}C$<br>-71  | A             |
| <b>Mounted on Large Heat Sink</b>                                 |                                           |                           |               |
| $I_{DP}^{①}$                                                      | Pulse Drain Current                       | $T_C=25^{\circ}C$<br>-284 | A             |
| $I_D^{②}$                                                         | Continuous Drain Current( $V_{GS}=-10V$ ) | $T_C=25^{\circ}C$<br>-71  | A             |
|                                                                   |                                           | $T_C=100^{\circ}C$<br>-50 |               |
| $P_D$                                                             | Maximum Power Dissipation                 | $T_C=25^{\circ}C$<br>62   | W             |
|                                                                   |                                           | $T_C=100^{\circ}C$<br>31  |               |
| $R_{\theta JC}$                                                   | Thermal Resistance-Junction to Case       | 2.4                       | $^{\circ}C/W$ |
| $R_{\theta JA}^{③}$                                               | Thermal Resistance-Junction to Ambient    | 100                       | $^{\circ}C/W$ |
| <b>Drain-Source Avalanche Ratings</b>                             |                                           |                           |               |
| $E_{AS}^{④}$                                                      | Avalanche Energy, Single Pulsed           | 306                       | mJ            |

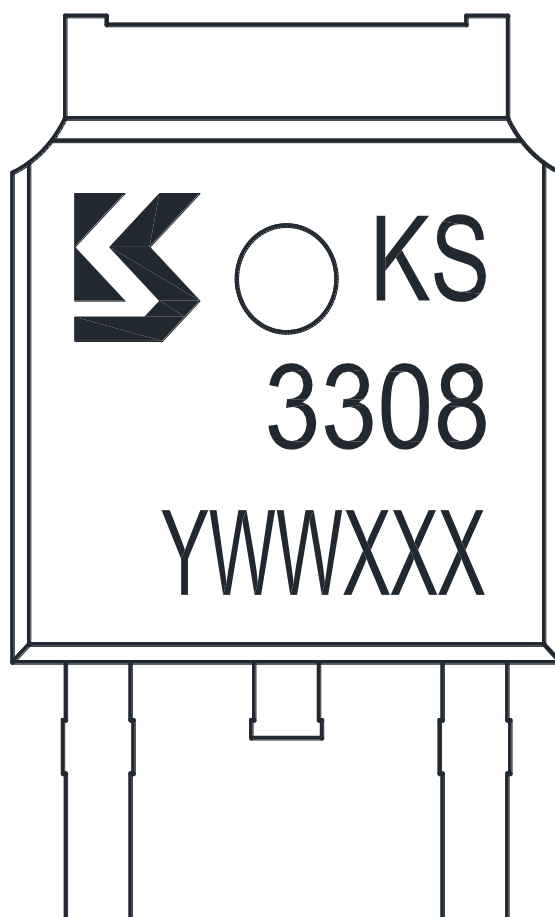
**Electrical Characteristics** ( $T_C=25^{\circ}\text{C}$  Unless Otherwise Noted)

| Symbol                                   | Parameter                        | Test Condition                                                                              | Rating |       |      | Unit |
|------------------------------------------|----------------------------------|---------------------------------------------------------------------------------------------|--------|-------|------|------|
|                                          |                                  |                                                                                             | Min.   | Typ.  | Max. |      |
| Static Characteristics                   |                                  |                                                                                             |        |       |      |      |
| BV <sub>DSS</sub>                        | Drain-Source Breakdown Voltage   | V <sub>GS</sub> =0V, I <sub>DS</sub> =-250μA                                                | -30    |       |      | V    |
| I <sub>DSS</sub>                         | Zero Gate Voltage Drain Current  | V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V                                                  |        |       | -1   | μA   |
|                                          |                                  | T <sub>J</sub> =125°C                                                                       |        |       | -30  |      |
| V <sub>GS(th)</sub>                      | Gate Threshold Voltage           | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =-250μA                                  | -1.1   | -1.5  | -2.3 | V    |
| I <sub>GSS</sub>                         | Gate Leakage Current             | V <sub>GS</sub> =±25V, V <sub>DS</sub> =0V                                                  |        |       | ±100 | nA   |
| R <sub>DS(ON)</sub> <sup>⑤</sup>         | Drain-Source On-state Resistance | V <sub>GS</sub> =-10V, I <sub>DS</sub> =-20A                                                |        | 6.6   | 8    | mΩ   |
|                                          |                                  | V <sub>GS</sub> =-4.5V, I <sub>DS</sub> =-16A                                               |        | 9     | 12   | mΩ   |
| Diode Characteristics                    |                                  |                                                                                             |        |       |      |      |
| V <sub>SD</sub> <sup>⑤</sup>             | Diode Forward Voltage            | I <sub>SD</sub> =-20A, V <sub>GS</sub> =0V                                                  |        | -0.87 | -1.2 | V    |
| t <sub>rr</sub>                          | Reverse Recovery Time            | I <sub>SD</sub> =-20A, dI <sub>SD</sub> /dt=-100A/μs                                        |        | 27    |      | ns   |
| Q <sub>rr</sub>                          | Reverse Recovery Charge          |                                                                                             |        | 58    |      | nC   |
| Dynamic Characteristics <sup>⑥</sup>     |                                  |                                                                                             |        |       |      |      |
| R <sub>G</sub>                           | Gate Resistance                  | V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz                                            |        | 6.6   |      | Ω    |
| C <sub>iss</sub>                         | Input Capacitance                | V <sub>GS</sub> =0V,<br>V <sub>DS</sub> =-15V,<br>Frequency=200KHz                          |        | 3400  |      | pF   |
| C <sub>oss</sub>                         | Output Capacitance               |                                                                                             |        | 385   |      |      |
| C <sub>rss</sub>                         | Reverse Transfer Capacitance     |                                                                                             |        | 350   |      |      |
| t <sub>d(ON)</sub>                       | Turn-on Delay Time               | V <sub>DD</sub> =-15V, I <sub>DS</sub> =-20A,<br>V <sub>GEN</sub> =-10V, R <sub>G</sub> =3Ω |        | 24    |      | ns   |
| t <sub>r</sub>                           | Turn-on Rise Time                |                                                                                             |        | 35    |      |      |
| t <sub>d(OFF)</sub>                      | Turn-off Delay Time              |                                                                                             |        | 57    |      |      |
| t <sub>f</sub>                           | Turn-off Fall Time               |                                                                                             |        | 31    |      |      |
| Gate Charge Characteristics <sup>⑥</sup> |                                  |                                                                                             |        |       |      |      |
| Q <sub>g</sub>                           | Total Gate Charge                | V <sub>DS</sub> =-15V, V <sub>GS</sub> =-10V,<br>I <sub>DS</sub> =-20A                      |        | 54    |      | nC   |
| Q <sub>gs</sub>                          | Gate-Source Charge               |                                                                                             |        | 9.5   |      |      |
| Q <sub>gd</sub>                          | Gate-Drain Charge                |                                                                                             |        | 13    |      |      |

- Notes:
- ① Pulse width limited by safe operating area.
  - ② Calculated continuous current based on maximum allowable junction temperature. The package limitation current is -60A.
  - ③ When mounted on 1 inch square copper board,  $t \leq 10\text{sec}$ . The value in any given application depends on the user's specific board design.
  - ④ Limited by  $T_{Jmax}$ , Starting  $T_J = 25^{\circ}\text{C}$ ,  $I_{ASmax} = -35A$ ,  $L=0.5\text{mH}$ ,  $V_{DD} = -20V$ ,  $R_G = 25\Omega$ ,  $V_{GS} = -10V$ . Part not recommended for use above this value. 100% Final Test at  $I_{AS} = -24A$ ,  $L=0.5\text{mH}$ .
  - ⑤ Pulse test; Pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ .
  - ⑥ Guaranteed by design, not subject to production testing.

## Ordering and Marking Information

| Device   | Package | Packaging | Quantity | Reel Size | Tape width |
|----------|---------|-----------|----------|-----------|------------|
| KS3308DA | TO-252  | Tape&Reel | 2500     | 13"       | 16mm       |

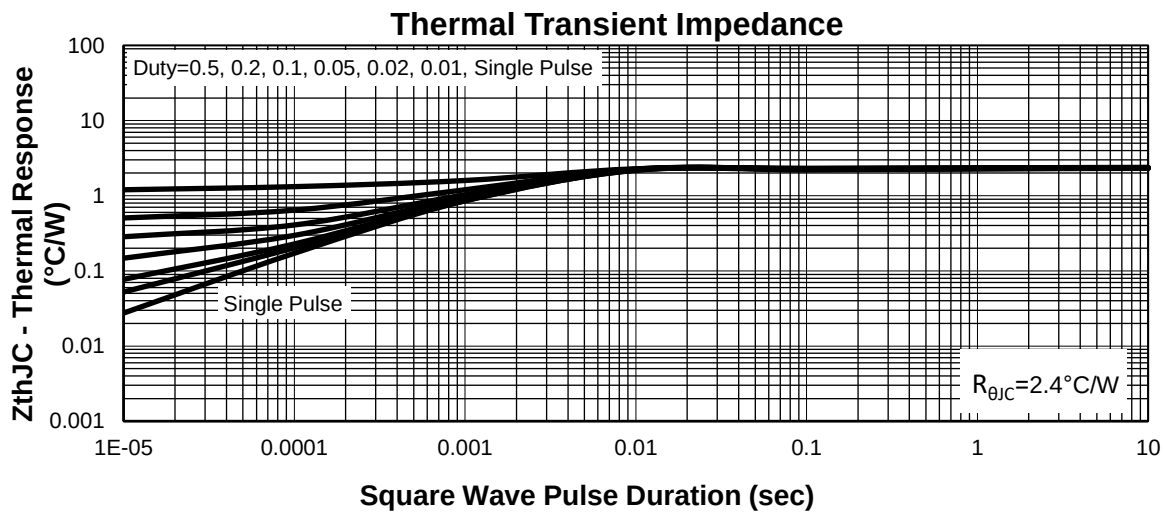
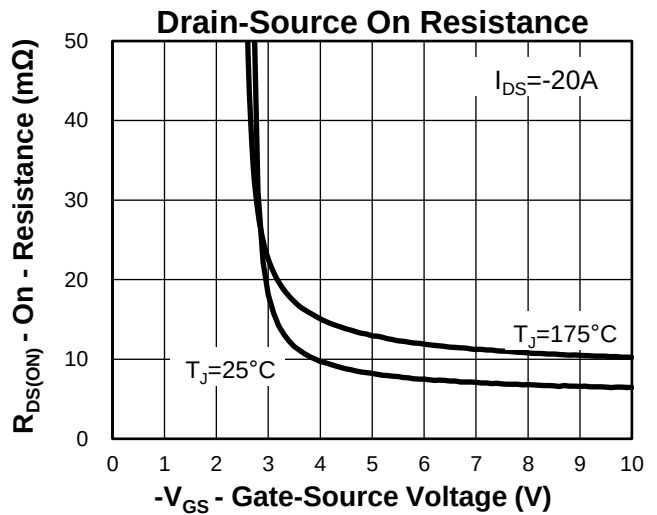
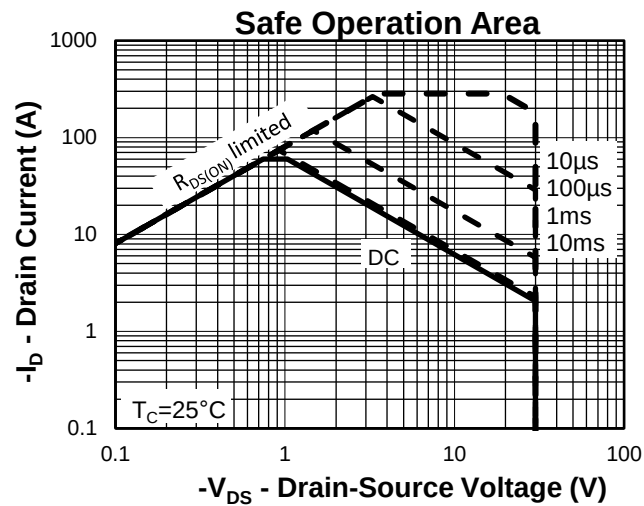
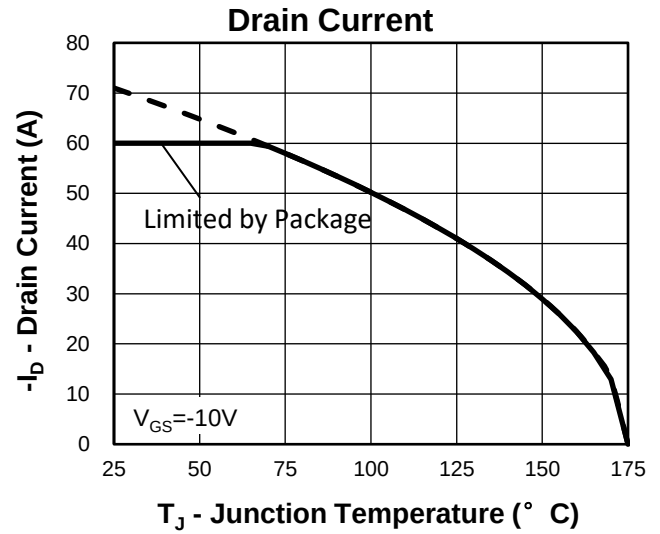
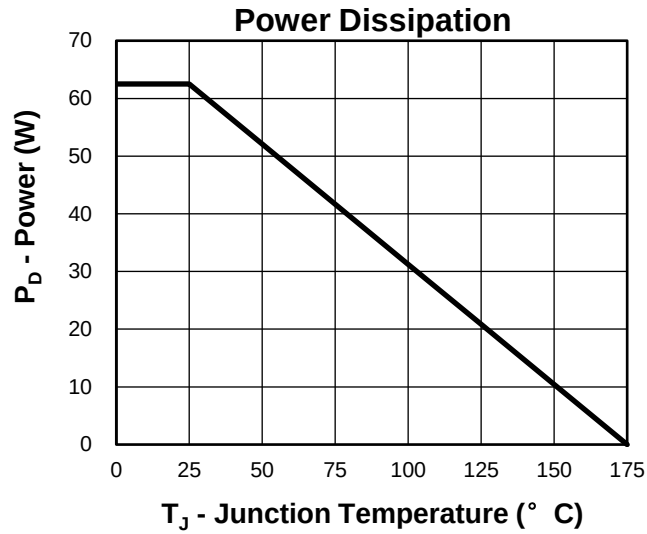


1st Line: Kwansemi LOGO, Kwansemi Code(KS)

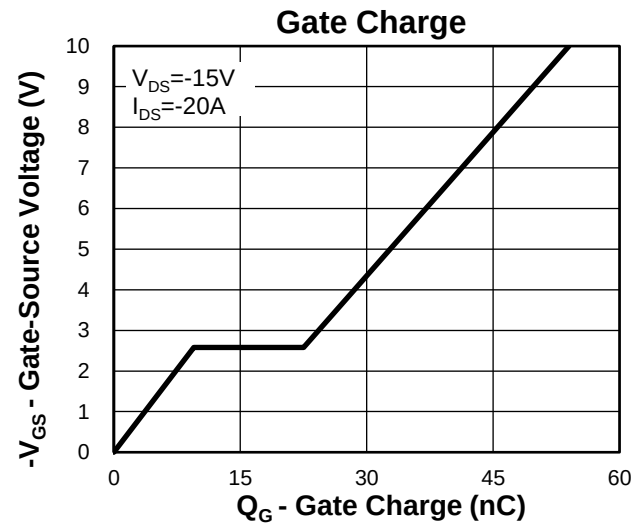
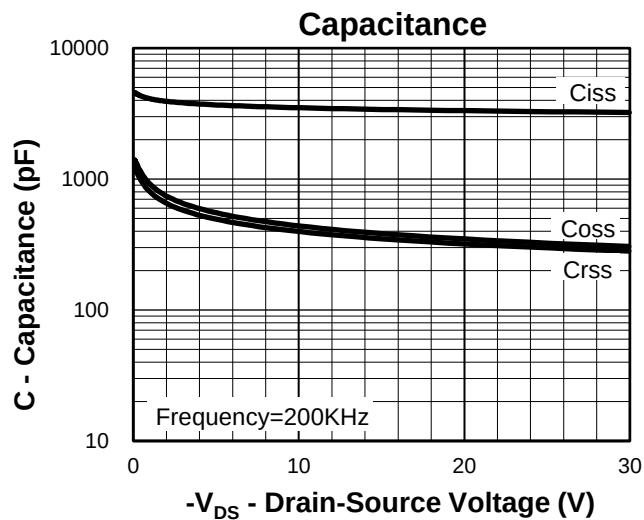
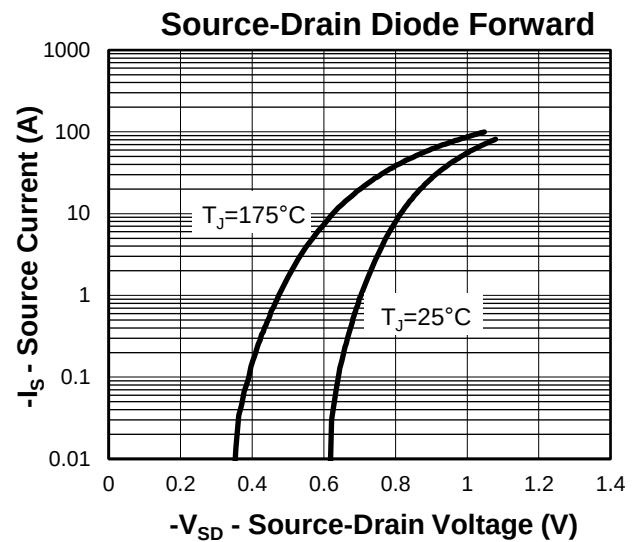
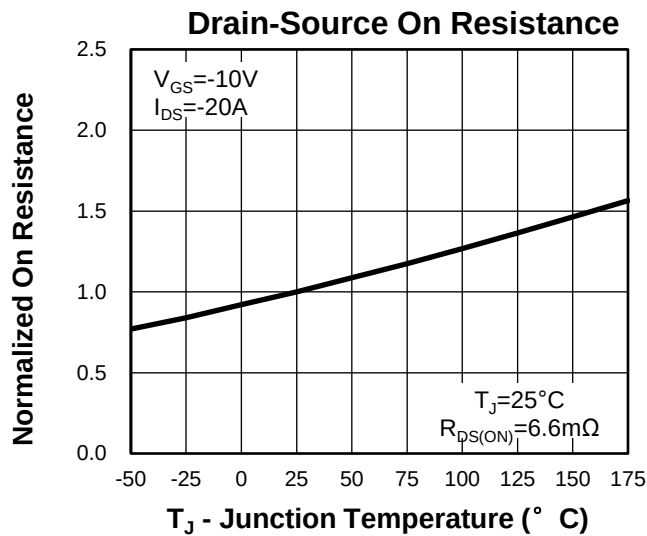
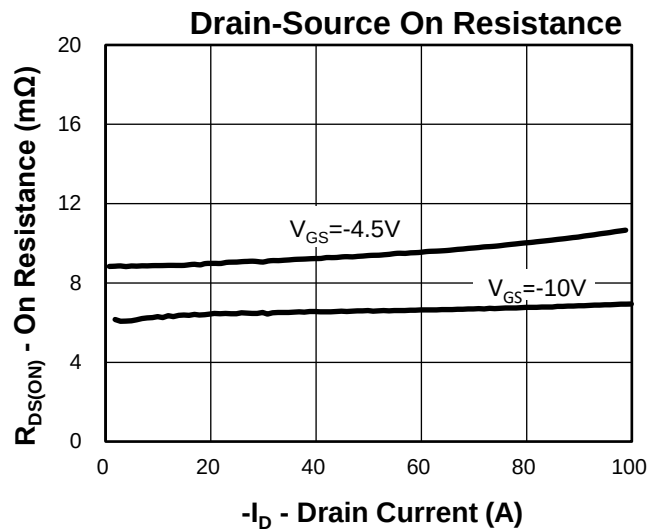
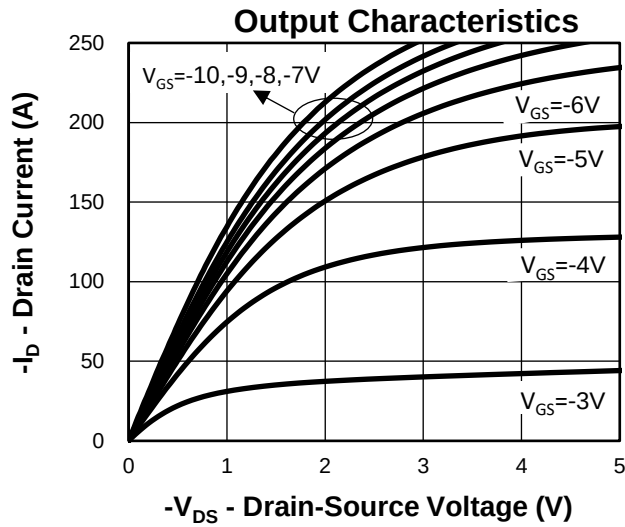
2nd Line: Part Number(3308)

3rd Line: Lot Number(YWWXXX)

## Typical Characteristics

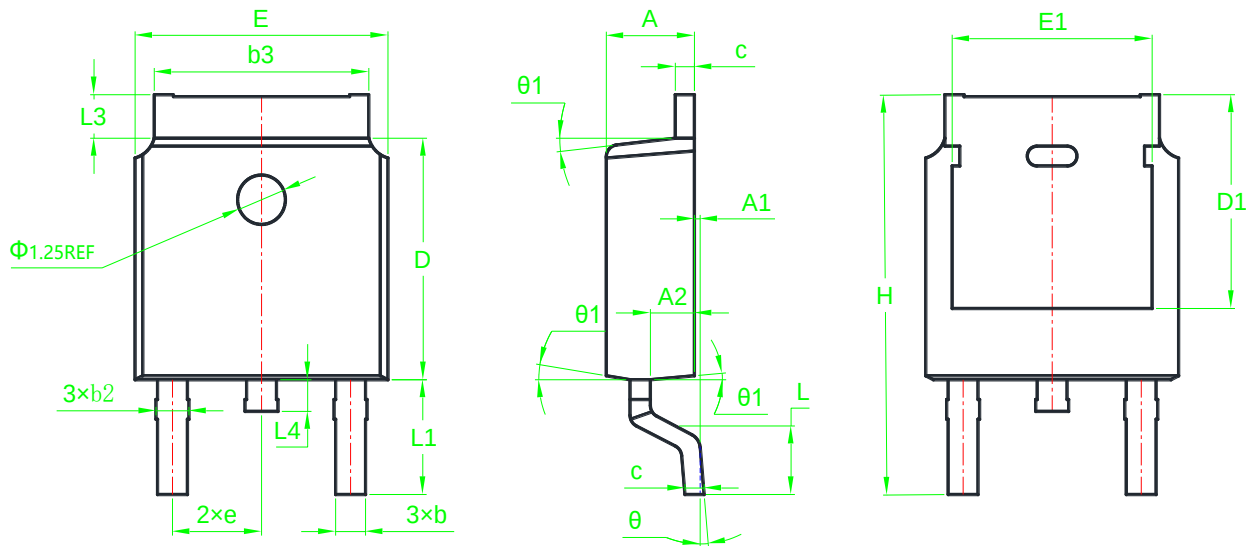


## Typical Characteristics



## Package Information

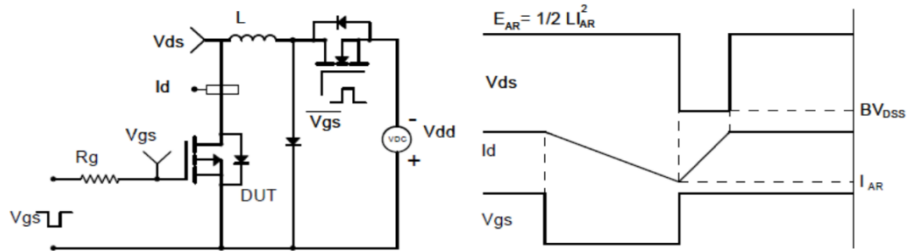
### TO-252



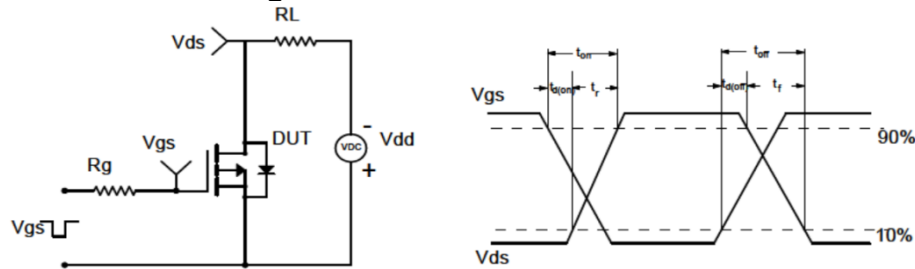
| SYMBOL | MM       |      |      | INCH     |       |       | SYMBOL | MM      |       |       | INCH     |       |       |
|--------|----------|------|------|----------|-------|-------|--------|---------|-------|-------|----------|-------|-------|
|        | MIN      | NOM  | MAX  | MIN      | NOM   | MAX   |        | MIN     | NOM   | MAX   | MIN      | NOM   | MAX   |
| A      | 2.20     | 2.30 | 2.38 | 0.087    | 0.091 | 0.094 | E      | 6.40    | 6.60  | 6.70  | 0.252    | 0.260 | 0.264 |
| A1     | 0.00     | *    | 0.15 | 0.000    | *     | 0.006 | E1     | 4.55    | *     | 5.15  | 0.179    | *     | 0.203 |
| A2     | 0.90     | 1.00 | 1.10 | 0.035    | 0.039 | 0.043 | H      | 9.60    | 10.10 | 10.40 | 0.378    | 0.398 | 0.409 |
| b      | 0.65     | 0.75 | 0.85 | 0.026    | 0.030 | 0.033 | L      | 1.40    | 1.50  | 1.70  | 0.055    | 0.059 | 0.067 |
| b2     | 0.72     | *    | 0.90 | 0.028    | *     | 0.035 | L1     | 2.90REF |       |       | 0.114REF |       |       |
| b3     | 5.13     | 5.33 | 5.46 | 0.202    | 0.210 | 0.215 | L3     | 0.90    | *     | 1.25  | 0.035    | *     | 0.049 |
| c      | 0.47     | 0.51 | 0.54 | 0.019    | 0.020 | 0.021 | L4     | 0.60    | *     | 1.00  | 0.024    | *     | 0.039 |
| D      | 6.00     | 6.10 | 6.20 | 0.236    | 0.240 | 0.244 | theta  | 0°      | *     | 10°   | 0°       | *     | 10°   |
| D1     | 5.25     | 5.35 | 5.60 | 0.207    | 0.211 | 0.220 | theta1 | 5°      | *     | 9°    | 5°       | *     | 9°    |
| e      | 2.286BSC |      |      | 0.090BSC |       |       |        |         |       |       |          |       |       |

Note: Dimensions do not inclusive burrs and mold flash.

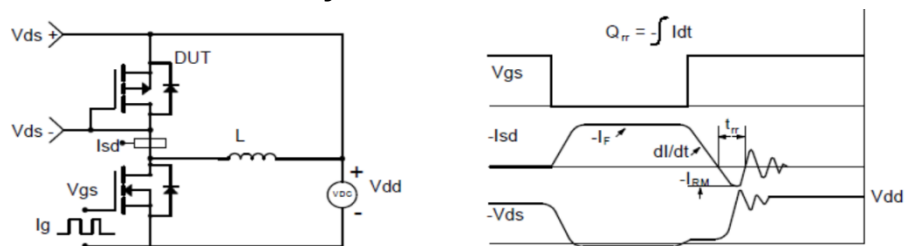
### Avalanche Test Circuit and Waveforms



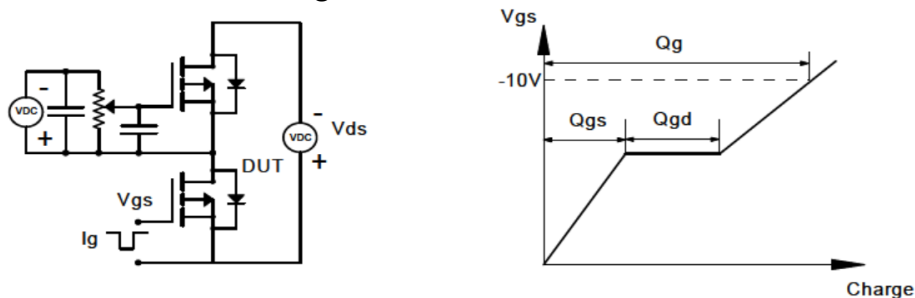
### Switching Time Test Circuit and Waveforms



### Diode Recovery Test Circuit and Waveforms



### Gate Charge Test Circuit and Waveform



### Customer Service

Kwansemi Semiconductor Co.,Ltd

Email:Sales@kwansemi.com

Web:www.kwansemi.com

### DISCLAIMER:

Kwansemi reserves the right to change the specifications and circuitry without notice at any time. The Products are not designed for use in hostile environments, including, without limitation, aircraft, nuclear power generation, medical appliances, and devices or systems in which malfunction of any Product can reasonably be expected to result in a personal injury. Seller's customers using or selling Seller's products for use in such applications do so at their own risk and agree to fully defend and indemnify Seller.