

Features

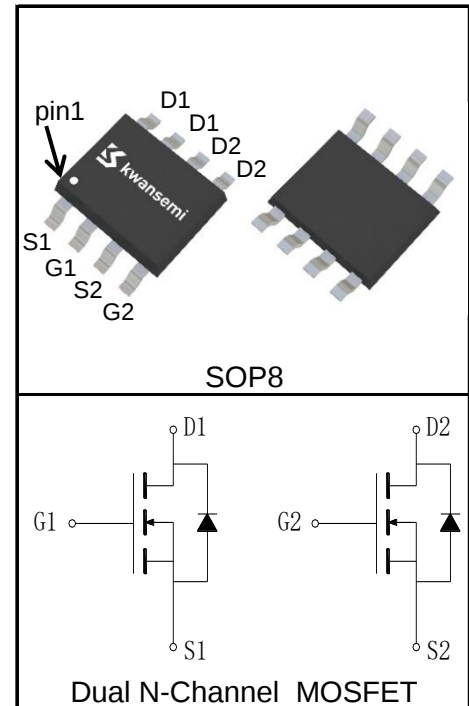
- 100V/6.7A,
 $R_{DS(ON)} = 20m\Omega(Typ.)@V_{GS}=10V$
 $R_{DS(ON)} = 24m\Omega(Typ.)@V_{GS}=4.5V$
- Excellent $Q_G \times R_{DS(on)}$ product(FOM)
- SGT Technology
- Fast Switching Speed
- Low Capacitance to Minimize Driver Losses

Applications

- Power Management
- Load Switch



Pin Description



Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (T _A =25°C Unless Otherwise Noted)				
V _{DSS}	Drain-Source Voltage		100	V
V _{GSS}	Gate-Source Voltage		±20	
T _{Jmax}	Maximum Junction Temperature		150	°C
T _J ,T _{STG}	Operating and Storage Temperature Range		-55 to 150	°C
I _S	Diode Continuous Forward Current	T _A =25°C	2.3	A
Mounted on Large Heat Sink				
I _{DP} ^①	Pulse Drain Current	T _A =25°C	26	A
I _D ^②	Continuous Drain Current(V _{GS} =10V)	T _A =25°C	6.7	A
		T _A =70°C	5.3	
P _D	Maximum Power Dissipation	T _A =25°C	2	W
		T _A =70°C	1.3	
R _{θJL}	Thermal Resistance-Junction to Lead		40	°C/W
R _{θJA} ^③	Thermal Resistance-Junction to Ambient		62.5	°C/W
Drain-Source Avalanche Ratings				
E _{AS} ^④	Avalanche Energy, Single Pulsed		56	mJ

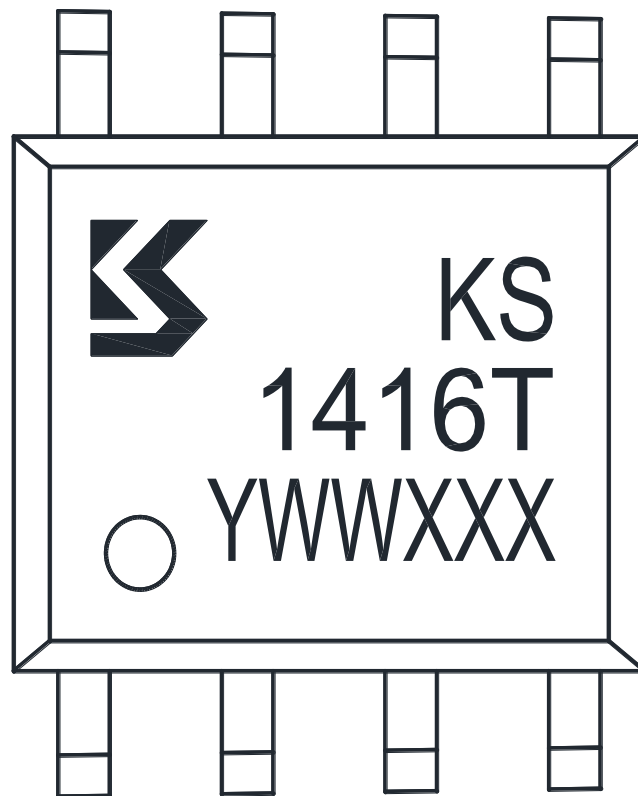
Electrical Characteristics ($T_A=25^{\circ}\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Condition	Rating			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	100			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =100V, V _{GS} =0V			1	μA
		T _J =125°C			30	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	1.2	1.8	2.3	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
R _{DS(ON)} ^⑤	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =6A		20	25	mΩ
		V _{GS} =4.5V, I _{DS} =4A		24	32	mΩ
Diode Characteristics						
V _{SD} ^⑤	Diode Forward Voltage	I _{SD} =6A, V _{GS} =0V		0.85	1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} =6A, dI _{SD} /dt=100A/μs		14		ns
Q _{rr}	Reverse Recovery Charge			25		nC
Dynamic Characteristics ^⑥						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz		5.7		Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =50V, Frequency=1.0MHz		640		pF
C _{oss}	Output Capacitance			215		
C _{rss}	Reverse Transfer Capacitance			4		
t _{d(ON)}	Turn-on Delay Time	V _{DD} =50V, I _{DS} =6A, V _{GEN} =10V, R _G =6Ω		10		ns
t _r	Turn-on Rise Time			13		
t _{d(OFF)}	Turn-off Delay Time			18		
t _f	Turn-off Fall Time			9		
Gate Charge Characteristics ^⑥						
Q _g	Total Gate Charge	V _{DS} =50V, V _{GS} =10V, I _{DS} =6A		12		nC
Q _{gs}	Gate-Source Charge			1.7		
Q _{gd}	Gate-Drain Charge			2.8		

- Notes:
- ① Pulse width limited by safe operating area.
 - ② Calculated continuous current based on maximum allowable junction temperature.
 - ③ When mounted on 1 inch square copper board, $t \leq 10\text{sec}$. The value in any given application depends on the user's specific board design.
 - ④ Limited by T_{Jmax} , Starting $T_J = 25^{\circ}\text{C}$, $I_{ASmax} = 15A$, $L = 0.5\text{mH}$, $V_{DD} = 48V$, $R_G = 25\Omega$, $V_{GS} = 10V$. Part not recommended for use above this value.
 - ⑤ Pulse test; Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
 - ⑥ Guaranteed by design, not subject to production testing.

Ordering and Marking Information

Device	Package	Packaging	Quantity	Reel Size	Tape width
KS1416HAT	SOP8	Tape&Reel	3000	13"	12mm

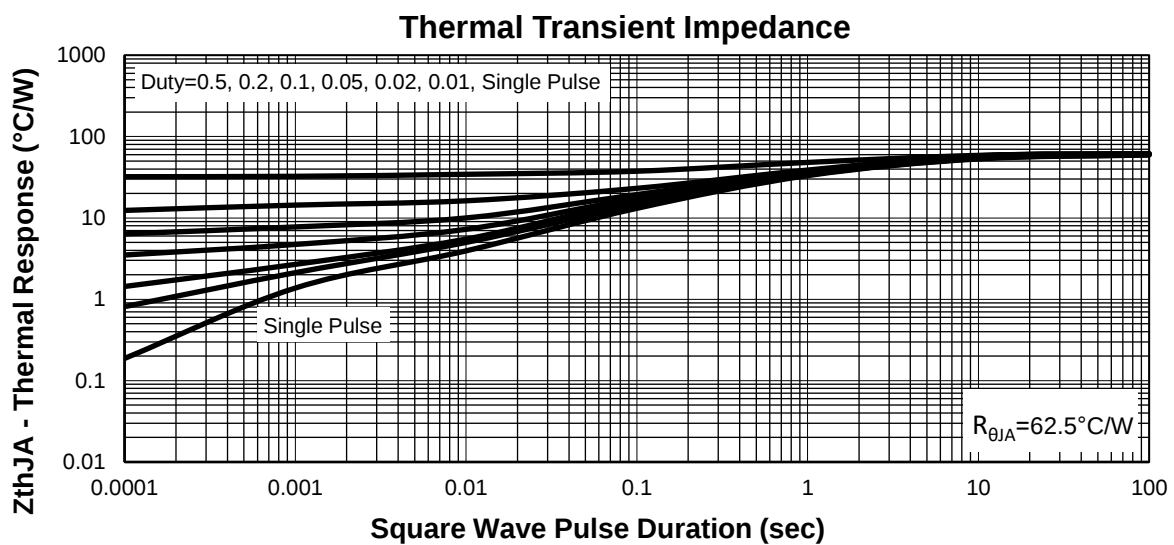
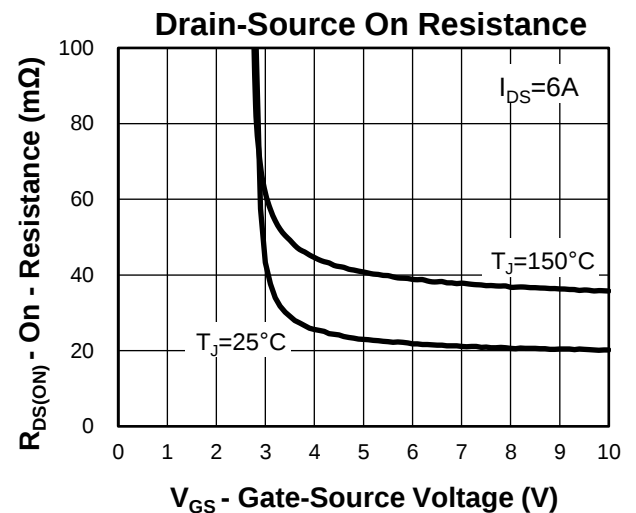
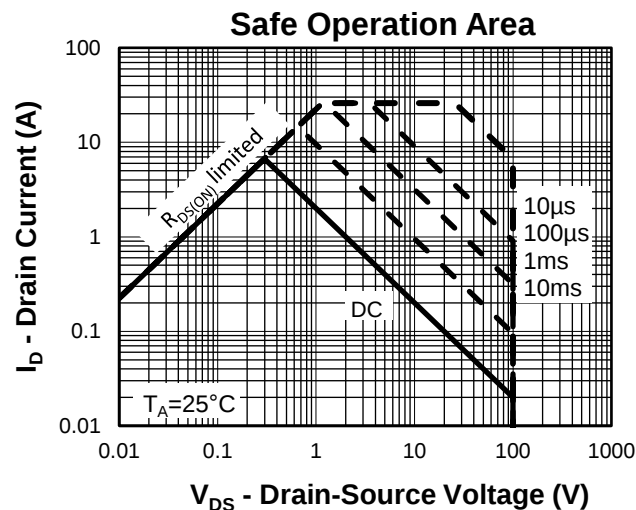
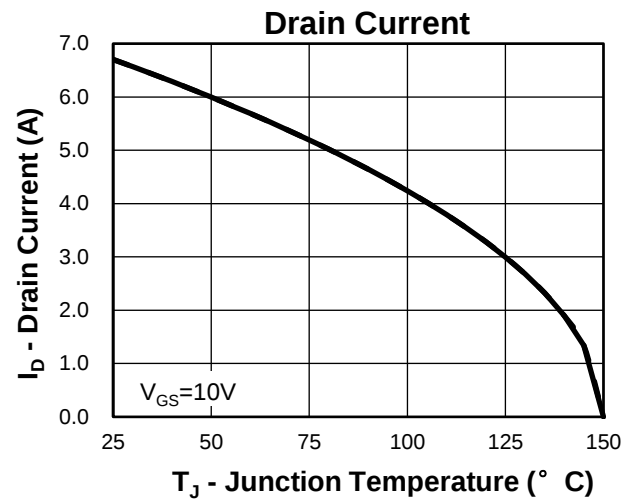
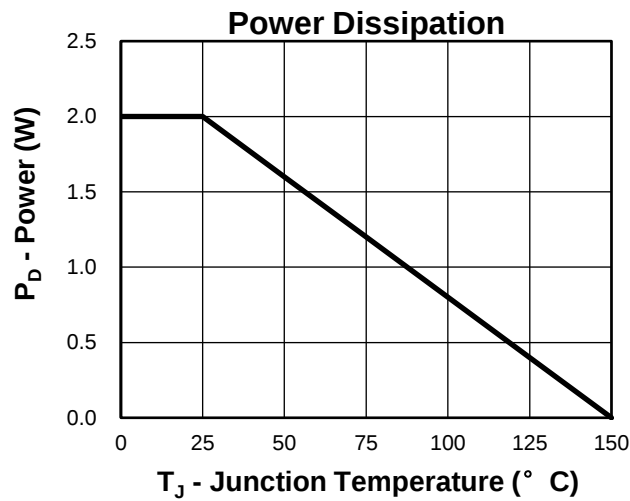


1st Line: Kwansemi LOGO, Kwansemi Code(KS)

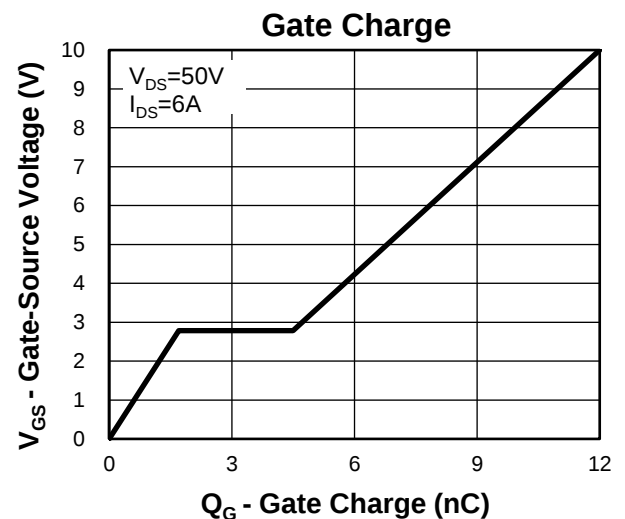
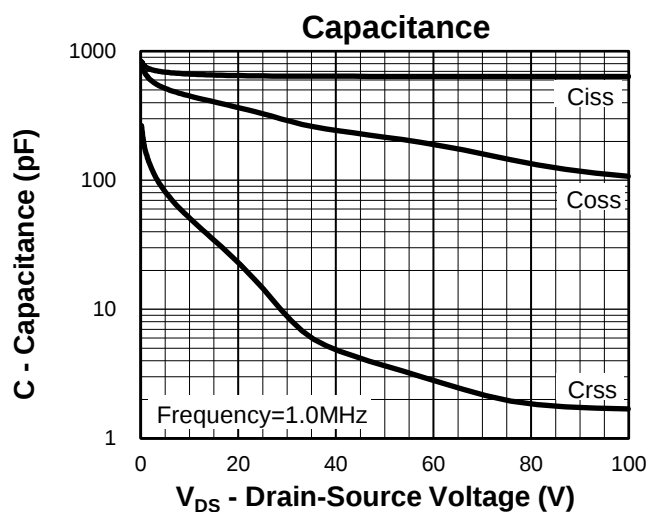
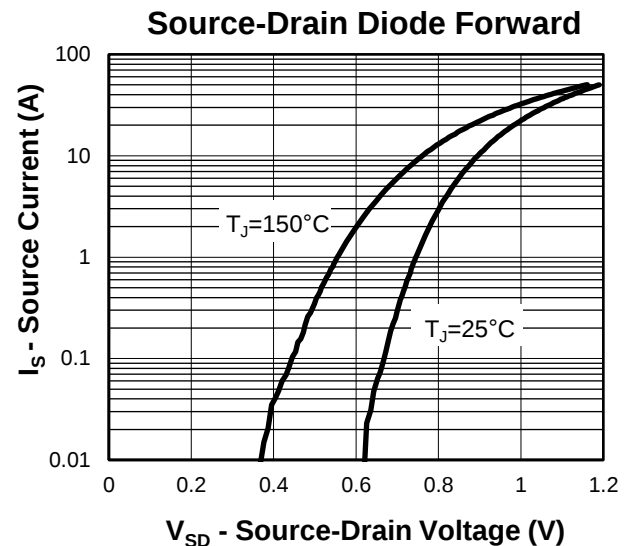
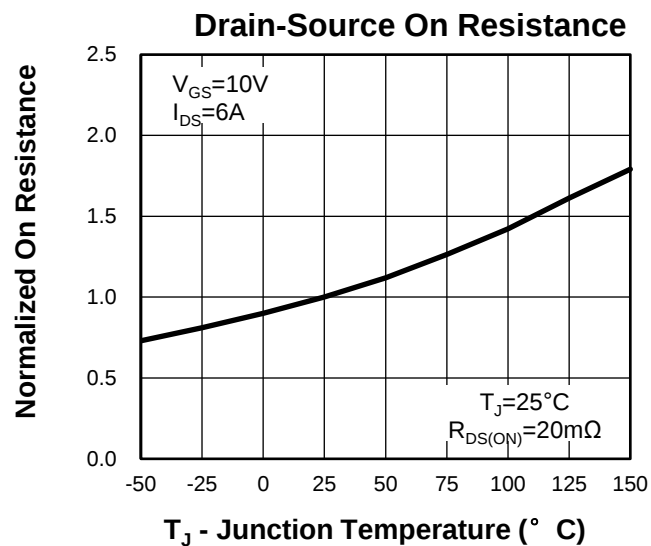
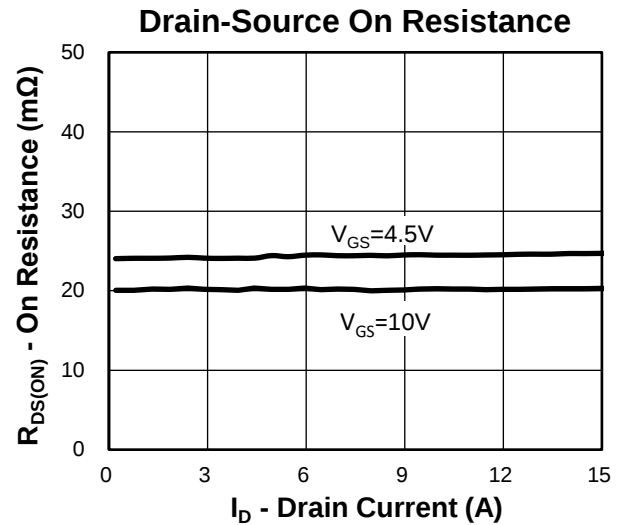
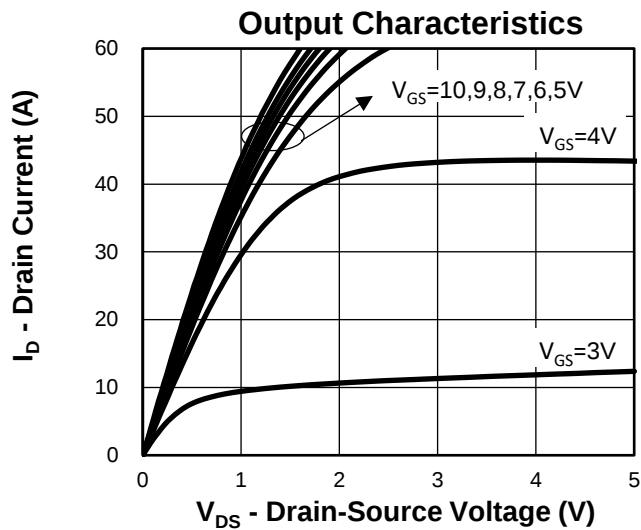
2nd Line: Part Number(1416T)

3rd Line: Lot Number(YWWXXX)

Typical Characteristics

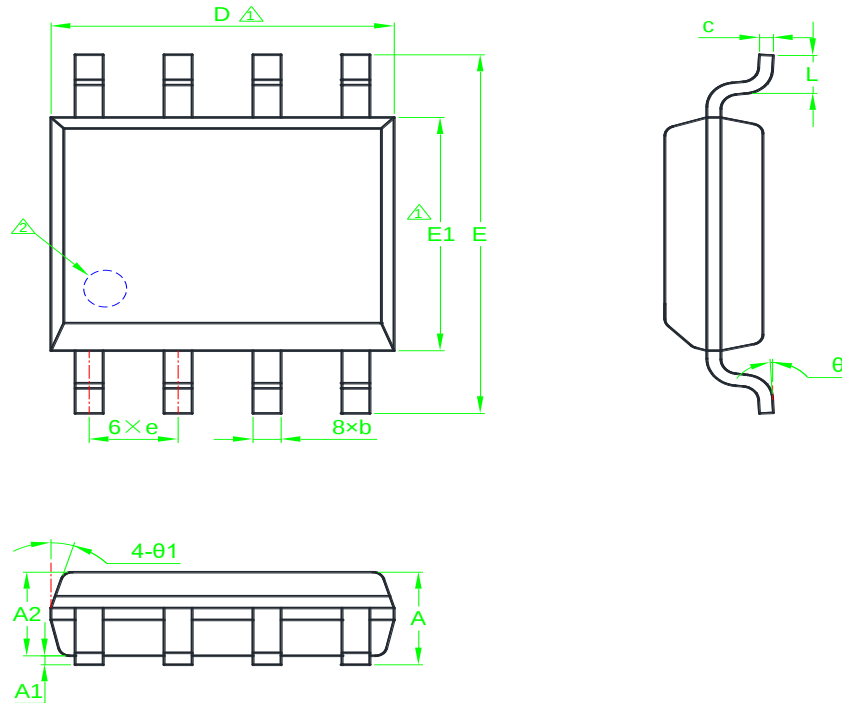


Typical Characteristics



Package Information

SOP8

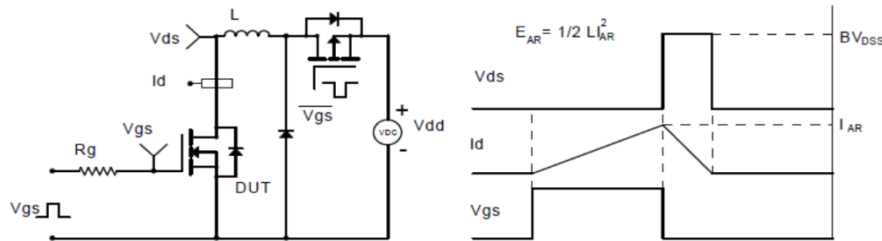


SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.35	*	1.75	0.053	*	0.069
A1	0.10	*	0.25	0.004	*	0.010
A2	1.25	1.45	1.65	0.049	0.057	0.065
b	0.33	*	0.51	0.013	*	0.020
c	0.15	*	0.25	0.006	*	0.010
D	4.70	4.90	5.10	0.185	0.193	0.201
E	5.80	6.00	6.30	0.228	0.236	0.248
E1	3.80	3.90	4.00	0.150	0.154	0.157
e	1.27BSC			0.050BSC		
L	0.40	*	1.27	0.016	*	0.050
θ	0°	*	8°	0°	*	8°
θ 1	5°	*	15°	5°	*	15°

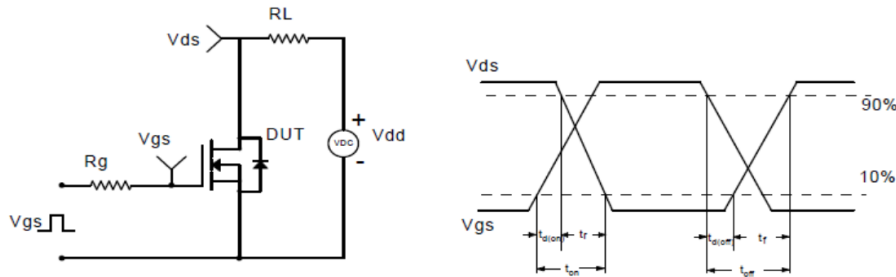
① Dimensions D and E1 do not include mold flash protrusions or gate burrs.

② The existence and size of demolding hole are variable depending on mold.

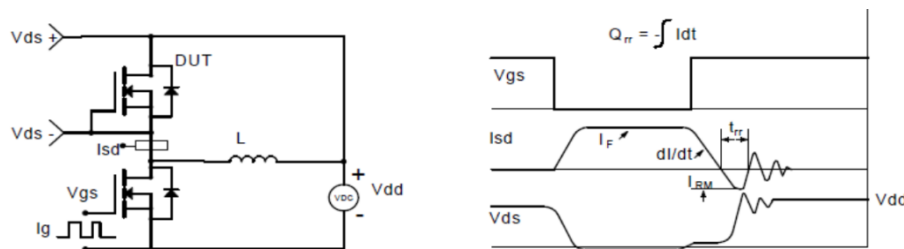
Avalanche Test Circuit and Waveforms



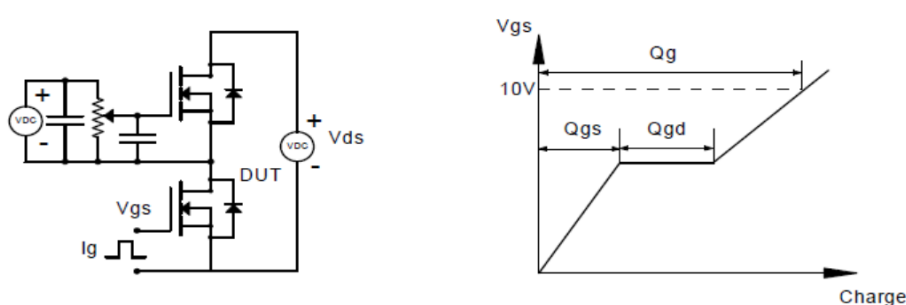
Switching Time Test Circuit and Waveforms



Diode Recovery Test Circuit and Waveforms



Gate Charge Test Circuit and Waveform



Customer Service

Kwansemi Semiconductor Co., Ltd

Email: Sales@kwansemi.com

Web: www.kwansemi.com

DISCLAIMER:

Kwansemi reserves the right to change the specifications and circuitry without notice at any time. The Products are not designed for use in hostile environments, including, without limitation, aircraft, nuclear power generation, medical appliances, and devices or systems in which malfunction of any Product can reasonably be expected to result in a personal injury. Seller's customers using or selling Seller's products for use in such applications do so at their own risk and agree to fully defend and indemnify Seller.